OSy group MiSmSv

Mário Mikula Robert Smetana Michal Švirec

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Teamwork

- We had team before signing up for OSy
- Leader is the most experienced in C
- Communication by irc, jabber
- Sessions at school

• Sources: Kalisto, HelenOS, lectures



Threads

- Pointers stored in AVL tree
- Only two queues for threads
 - Running threads (scheduled)
 - Detached threads, unallocated on reschedule
- FIFO scheduling algorithm
 - Uses timers



Timers

- Priority queue for active timers
- Invoked timers queue
 - Multiple timers can be invoked in one interrupt
- Timer interrupts handled in special handler thread
 - Remove timer from invoked queue
 - Execute callback method
 - Everything with enabled interrupts



Frame allocator

- Bitmap of entire physical memory
 - Both mapped and unmapped
 - Write checked on startup
- Kernel heap allocated from the "front"
 - Continuous block, can be enlarged/shrunk
 - Next fit strategy
- Everything else allocated from the "end"
 - First fit strategy



Kernel heap

- Continuous in physical memory
- Constant initial size
- Size increases by constant when needed
- Decreases only from the tail
 - Not ideal in special case

Memory management

- Initially we implemented one level paging
 - Every thread used > 4MB of memory
 - Map2 tests needed > 20MB 😳
- Implemented 2 level paging
 - Memory requirements down to 280k
 - Realized the power of multilevel paging



- First fit strategy
- Sorted array of areas by start address

- Not a final version
- We are working on 3rd assignment



Next assignments

- Fixed number of registers for syscalls
 - Syscall number
 - 4 regs. for parameters
- Buffering for functions that output text



Deadly traps

 $\stackrel{\$}{_{\sim}}$ Don't allow timers only a few μs apart

Double check values read/written to CP0 regs.

Reserve more time for debugging

Disable and enable interrupts on proper place

if (!thread) return ENOMEM;	290 291 292		293 294 295	<pre>// Initialize. init_thread(thread, thread_start, data);</pre>
<pre>// Initialize. init_thread(thread, thread_start, data);</pre>	293 294		296 297	<pre>ipl_t state = query_and_disable_interrupts();</pre>
	295	\checkmark	298	if (flags & TF_NEW_VMM)
if (flags & TF_NEW_VMM)	296		299	{ // create new virtual address space
{ // create new virtual address space	297		300	<pre>thread->paging = paging_init();</pre>
thread->paging = paging_init();	298		301	if (thread->paging == NULL)
if (thread->paging == NULL)	299		302	{
{	300		303	free(thread);
<pre>free(thread);</pre>	301	\square	304	<pre>conditionally_enable_interrupts(state);</pre>
return ENOMEM;	302		305	return ENOMEM;
}	303		306	}
// thread has new ASID	304		307	// thread has new ASID
<pre>thread->paging->asid = thread->id;</pre>	305		308	<pre>thread->paging->asid = thread->id;</pre>
}	306		309	}
else	307		310	else
<pre>{// inherit virtual address space from parent</pre>	308		311	{// inherit virtual address space from parent
<pre>thread->paging = thread_get_current()->paging;</pre>	309		312	<pre>thread->paging = thread_get_current()->pagin</pre>
thread->paging->ref_count += 1:	310		313	thread->paging->ref_count += 1;
}	311		314	}
	312		315	
	313	7	316	avltree_insert(&threads_tree, &thread->threads_
<pre>ipl_t state = query_and_disable_interrupts();</pre>	314		317	<pre>conditionally_enable_interrupts(state);</pre>
avltree_insert(&threads_tree, &thread->threads_tre	315		318	
<pre>conditionally_enable_interrupts(state);</pre>	316		319	// And run.
<i>y x y y y y</i>	317		320	thread->status = RUNNABLE;
// And run.	318		321	runnable(thread);
thread->status = RUNNABLE;	319		322	

Thank You