cycles per instruction \[1\]: LEC - avrora (uncontrolled) and avrora (controlled), "best" pinning

uncontrolled throughput \[1\]: LEC - avrora (uncontrolled) and avrora (controlled), "best" pinning

controlled throughput \[1\]: LEC - avrora (uncontrolled) and avrora (controlled), "best" pinning

cycles per instruction \[1\]: LEC - avrora (uncontrolled) and avrora (controlled), "worst" pinning

power consumption \[1\]: LEC - avrora (uncontrolled) and avrora (controlled), "best" pinning

power consumption \[1\]: LEC - avrora (uncontrolled) and avrora (controlled), "worst" pinning
cycles per instruction [1] : L2C : luindex (uncontrolled) and space (controlled), "best" pinning

uncontrolled throughput [1]: L2C : luindex (uncontrolled) and space (controlled), "best" pinning

controlled throughput [1]: L2C : luindex (uncontrolled) and space (controlled), "best" pinning

cycles per instruction [1] : L2C : luindex (uncontrolled) and space (controlled), "worst" pinning

uncontrolled throughput [1]: L2C : luindex (uncontrolled) and space (controlled), "worst" pinning

controlled throughput [1]: L2C : luindex (uncontrolled) and space (controlled), "worst" pinning

cycles per instruction [1] : L2C : luindex (uncontrolled) and space (controlled), "worst" pinning

power consumption [W]: L2C : luindex (uncontrolled) and space (controlled), "best" pinning

power consumption [W]: L2C : luindex (uncontrolled) and space (controlled), "worst" pinning

power consumption [W]: L2C : luindex (uncontrolled) and space (controlled), "worst" pinning
cycles per instruction [1] : L1C : scale (uncontrolled) and bidx (controlled), "best" pinning

uncontrolled throughput [1] : L1C : scale (uncontrolled) and bidx (controlled), "best" pinning

controlled throughput [1] : L1C : scale (uncontrolled) and bidx (controlled), "best" pinning

cycles per instruction [1] : L2C : scale (uncontrolled) and bidx (controlled), "worst" pinning

uncontrolled throughput [1] : L2C : scale (uncontrolled) and bidx (controlled), "worst" pinning

controlled throughput [1] : L2C : scale (uncontrolled) and bidx (controlled), "worst" pinning

power consumption [W] : L1C : scale (uncontrolled) and bidx (controlled), "best" pinning

uncontrolled throughput [1] : L1C : scale (uncontrolled) and bidx (controlled), "worst" pinning

cycles per instruction [1] : L2C : scale (uncontrolled) and bidx (controlled), "worst" pinning

uncontrolled throughput [1] : L2C : scale (uncontrolled) and bidx (controlled), "worst" pinning

power consumption [W] : L2C : scale (uncontrolled) and bidx (controlled), "worst" pinning