uncontrolled throughput [1/s] : KVM : avrora (uncontrolled) and avrora (controlled), "neutral" pinning

cycles per instruction [1] : KVM : avrora (uncontrolled) and avrora (controlled), "neutral" pinning
cycles per instruction [1] : KVM : luindex (uncontrolled) and avrora (controlled), "neutral" pinning

uncontrolled throughput [1/s] : LXC : luindex (uncontrolled) and avrora (controlled), "worse" pinning
cycles per instruction [1] : KVM : luindex (uncontrolled) and h2 (controlled), "neutral" pinning

uncontrolled throughput [1/s] : LXC : luindex (uncontrolled) and h2 (controlled), "worst" pinning

staircase trace time [s]
cycles per instruction [1] : KVM : scalac (uncontrolled) and h2 (controlled), “neutral” pinning
cycles per instruction \([1]\) : KVM : scalac (uncontrolled) and luindex (controlled), "neutral" pinning

cycles per instruction \([1]\) : KVM : scalac (uncontrolled) and luindex (controlled), "worse" pinning
cycles per instruction [1]: KVM: scalac (uncontrolled) and scalac (controlled), "neutral" pinning

cycles per instruction [1]: LXC: scalac (uncontrolled) and scalac (controlled), "best" pinning
cycles per instruction [1]: KVM: scalac (uncontrolled) and specs (controlled), "neutral" pinning.
cycles per instruction [1] : KVM : specs (uncontrolled) and h2 (controlled), “neutral” pinning

uncontrolled throughput [1/s] : LXC : specs (uncontrolled) and h2 (controlled), “worst” pinning