cycles per instruction \[1\] : KVM : avrora (uncontrolled) and luindex (controlled), "neutral" pinning

uncontrolled throughput \[1/s\]
cycles per instruction [1] : KVM : avrora (uncontrolled) and scalac (controlled), "neutral" pinning

uncontrolled throughput [1/s] : LXC : avrora (uncontrolled) and scalac (controlled), "worst" pinning
cycles per instruction [1] : KVM : luindex (uncontrolled) and luindex (controlled), "neutral" pinning
uncontrolled throughput [1/s] : LXC : luindex (uncontrolled) and luindex (controlled), "worse" pinning