cycles per instruction [1]: LXC : avrora (uncontrolled) and h2 (controlled), "best" pinning

cycles per instruction [1]: LXC : avrora (uncontrolled) and h2 (controlled), "worst" pinning

uncontrolled throughput [1/s]: LXC : avrora (uncontrolled) and h2 (controlled), "best" pinning

uncontrolled throughput [1/s]: LXC : avrora (uncontrolled) and h2 (controlled), "worst" pinning

controlled throughput [1/s]: LXC : avrora (uncontrolled) and h2 (controlled), "best" pinning

controlled throughput [1/s]: LXC : avrora (uncontrolled) and h2 (controlled), "worst" pinning
cycles per instruction [1/s]: LXC: avrora (uncontrolled) and luindex (controlled), "best" pinning

uncontrolled throughput [1/s]: LXC: avrora (uncontrolled) and luindex (controlled), "best" pinning

controlled throughput [1/s]: LXC: avrora (uncontrolled) and luindex (controlled), "best" pinning

cycles per instruction [1/s]: LXC: avrora (uncontrolled) and luindex (controlled), "worst" pinning

uncontrolled throughput [1/s]: LXC: avrora (uncontrolled) and luindex (controlled), "worst" pinning

controlled throughput [1/s]: LXC: avrora (uncontrolled) and luindex (controlled), "worst" pinning
cycles per instruction \[1\] : LXC : h2 (uncontrolled) and scalac (controlled), "best" pinning

cycles per instruction \[1\] : LXC : h2 (uncontrolled) and scalac (controlled), "worse" pinning

uncontrolled throughput \[1\] : LXC : h2 (uncontrolled) and scalac (controlled), "best" pinning

uncontrolled throughput \[1\] : LXC : h2 (uncontrolled) and scalac (controlled), "worse" pinning

controlled throughput \[1\] : LXC : h2 (uncontrolled) and scalac (controlled), "best" pinning

controlled throughput \[1\] : LXC : h2 (uncontrolled) and scalac (controlled), "worse" pinning
cycles per instruction [1]: LXC : h2 (uncontrolled) and specs (controlled), “best” pinning

cycles per instruction [1]: LXC : h2 (uncontrolled) and specs (controlled), “worst” pinning

uncontrolled throughput [1/a]: LXC : h2 (uncontrolled) and specs (controlled), “best” pinning

uncontrolled throughput [1/a]: LXC : h2 (uncontrolled) and specs (controlled), “worst” pinning

controlled throughput [1/a]: LXC : h2 (uncontrolled) and specs (controlled), “best” pinning

controlled throughput [1/a]: LXC : h2 (uncontrolled) and specs (controlled), “worst” pinning
cycles per instruction [1]: LXC : scalac (uncontrolled) and h2 (controlled), "best" pinning

cycles per instruction [1]: LXC : scalac (uncontrolled) and h2 (controlled), "worse" pinning

uncontrolled throughput [1/s]: LXC : scalac (uncontrolled) and h2 (controlled), "best" pinning

uncontrolled throughput [1/s]: LXC : scalac (uncontrolled) and h2 (controlled), "worse" pinning

controlled throughput [%]: LXC : scalac (uncontrolled) and h2 (controlled), "best" pinning

controlled throughput [%]: LXC : scalac (uncontrolled) and h2 (controlled), "worse" pinning
cycles per instruction [1] : LXC : scalac (uncontrolled) and luindex (controlled), "best" pinning

cycles per instruction [1] : LXC : scalac (uncontrolled) and luindex (controlled), "worse" pinning

uncontrolled throughput [1/s] : LXC : scalac (uncontrolled) and luindex (controlled), "best" pinning

uncontrolled throughput [1/s] : LXC : scalac (uncontrolled) and luindex (controlled), "worse" pinning

controlled throughput [1/s] : LXC : scalac (uncontrolled) and luindex (controlled), "best" pinning

controlled throughput [1/s] : LXC : scalac (uncontrolled) and luindex (controlled), "worse" pinning

controlled throughput [1/s] : LXC : scalac (uncontrolled) and luindex (controlled), "worst" pinning

controlled throughput [1/s] : LXC : scalac (uncontrolled) and luindex (controlled), "worst" pinning
cycles per instruction \([1]\) : LXC : scalac (uncontrolled) and scalac (controlled), "best" pinning

cycles per instruction \([1]\) : LXC : scalac (uncontrolled) and scalac (controlled), "worst" pinning

uncontrolled throughput \([1/s]\) : LXC : scalac (uncontrolled) and scalac (controlled), "best" pinning

uncontrolled throughput \([1/s]\) : LXC : scalac (uncontrolled) and scalac (controlled), "worst" pinning

controlled throughput \([1/s]\) : LXC : scalac (uncontrolled) and scalac (controlled), "best" pinning

controlled throughput \([1/s]\) : LXC : scalac (uncontrolled) and scalac (controlled), "worst" pinning

staircase trace time \([s]\)
cycles per instruction [1]: LXC : specs (uncontrolled) and avrora (controlled), "best" pinning

cycles per instruction [1]: LXC : specs (uncontrolled) and avrora (controlled), "worse" pinning

uncontrolled throughput [1/s]: LXC : specs (uncontrolled) and avrora (controlled), "best" pinning

uncontrolled throughput [1/s]: LXC : specs (uncontrolled) and avrora (controlled), "worse" pinning

controlled throughput [1/s]: LXC : specs (uncontrolled) and avrora (controlled), "best" pinning

controlled throughput [1/s]: LXC : specs (uncontrolled) and avrora (controlled), "worse" pinning
cycles per instruction [1]: LXC : specs (uncontrolled) and scalac (controlled), "best" pinning

cycles per instruction [1]: LXC : specs (uncontrolled) and scalac (controlled), "worse" pinning

uncontrolled throughput [1/a]: LXC : specs (uncontrolled) and scalac (controlled), "best" pinning

uncontrolled throughput [1/a]: LXC : specs (uncontrolled) and scalac (controlled), "worse" pinning

controlled throughput [1/a]: LXC : specs (uncontrolled) and scalac (controlled), "best" pinning

controlled throughput [1/a]: LXC : specs (uncontrolled) and scalac (controlled), "worse" pinning