cycles per instruction [1]: KVM: h2 (uncontrolled) and scalac (controlled), "neutral" pinning

power consumption [W]: LXC: h2 (uncontrolled) and scalac (controlled), "worst" pinning
cycles per instruction [1] : KVM : scalac (uncontrolled) and avrora (controlled), “neutral” pinning
cycles per instruction [1]: KVM: scalac (uncontrolled) and h2 (controlled), "neutral" pinning