cycles per instruction [1] : KVM : avrora (uncontrolled) and specs (controlled), "neutral" pinning

controlled throughput [1/s]
staircase trace time [s]

staircase trace time [s]
cycles per instruction \[1\] : KVM : h2 (uncontrolled) and h2 (controlled), "neutral" pinning
cycles per instruction \[1\] : KVM : scalac (uncontrolled) and specs (controlled), "neutral" pinning