cycles per instruction [1] - LXC : avrora (uncontrolled) and h2 (controlled), "best" pinning

cycles per instruction [1] - LXC : avrora (uncontrolled) and h2 (controlled), "worse" pinning

uncontrolled throughput [1] - LXC : avrora (uncontrolled) and h2 (controlled), "best" pinning

uncontrolled throughput [1] - LXC : avrora (uncontrolled) and h2 (controlled), "worse" pinning

controlled throughput [1] - LXC : avrora (uncontrolled) and h2 (controlled), "best" pinning

controlled throughput [1] - LXC : avrora (uncontrolled) and h2 (controlled), "worse" pinning

power consumption [W] - LXC : avrora (uncontrolled) and h2 (controlled), "best" pinning

power consumption [W] - LXC : avrora (uncontrolled) and h2 (controlled), "worse" pinning

uncontrolled throughput [1] - LXC : avrora (uncontrolled) and h2 (controlled), "best" pinning

uncontrolled throughput [1] - LXC : avrora (uncontrolled) and h2 (controlled), "worse" pinning

controlled throughput [1] - LXC : avrora (uncontrolled) and h2 (controlled), "best" pinning

controlled throughput [1] - LXC : avrora (uncontrolled) and h2 (controlled), "worse" pinning

power consumption [W] - LXC : avrora (uncontrolled) and h2 (controlled), "best" pinning

power consumption [W] - LXC : avrora (uncontrolled) and h2 (controlled), "worse" pinning

uncontrolled throughput [1] - LXC : avrora (uncontrolled) and h2 (controlled), "best" pinning

uncontrolled throughput [1] - LXC : avrora (uncontrolled) and h2 (controlled), "worse" pinning

controlled throughput [1] - LXC : avrora (uncontrolled) and h2 (controlled), "best" pinning

controlled throughput [1] - LXC : avrora (uncontrolled) and h2 (controlled), "worse" pinning

power consumption [W] - LXC : avrora (uncontrolled) and h2 (controlled), "best" pinning

power consumption [W] - LXC : avrora (uncontrolled) and h2 (controlled), "worse" pinning
cycles per instruction [1]: LXC: scales (uncontrolled) and h2 (controlled), "best" pinning

cycles per instruction [2]: LXC: scales (uncontrolled) and h2 (controlled), "worse" pinning

cycles per instruction [3]: LXC: scales (uncontrolled) and h2 (controlled), "worse" pinning

uncontrolled throughput [1]: LXC: scales (uncontrolled) and h2 (controlled), "best" pinning

uncontrolled throughput [4]: LXC: scales (uncontrolled) and h2 (controlled), "worse" pinning

cycled throughout [1]: LXC: scales (uncontrolled) and h2 (controlled), "best" pinning

cycled throughout [4]: LXC: scales (uncontrolled) and h2 (controlled), "better" pinning

cycled throughout [4]: LXC: scales (uncontrolled) and h2 (controlled), "worse" pinning

power consumption [1]: LXC: scales (uncontrolled) and h2 (controlled), "best" pinning

power consumption [4]: LXC: scales (uncontrolled) and h2 (controlled), "worse" pinning

power consumption [4]: LXC: scales (uncontrolled) and h2 (controlled), "worse" pinning
cycles per instruction [1]: L1C: scalac (uncontrolled) and lindex (controlled), "best" pinning

uncontrolled throughput [1]: L1C: scalac (uncontrolled) and lindex (controlled), "best" pinning

controlled throughput [1]: L1C: scalac (uncontrolled) and lindex (controlled), "best" pinning

power consumption [W]: L1C: scalac (uncontrolled) and lindex (controlled), "best" pinning