cycles per instruction [1] : LXC : avrora (uncontrolled) and avrora (controlled), "best" pinning

uncontrolled throughput [1] : LXC : avrora (uncontrolled) and avrora (controlled), "best" pinning

controlled throughput [1] : LXC : avrora (uncontrolled) and avrora (controlled), "best" pinning

cycles per instruction [1] : LXC : avrora (uncontrolled) and avrora (controlled), "worse" pinning

uncontrolled throughput [1] : LXC : avrora (uncontrolled) and avrora (controlled), "worse" pinning

controlled throughput [1] : LXC : avrora (uncontrolled) and avrora (controlled), "worse" pinning

power consumption [W] : LXC : avrora (uncontrolled) and avrora (controlled), "best" pinning

power consumption [W] : LXC : avrora (uncontrolled) and avrora (controlled), "worse" pinning
cycles per instruction [1]: LEC: avrora (uncontrolled) and luindex (controlled), "best" pinning

cycles per instruction [2]: LEC: avrora (uncontrolled) and luindex (controlled), "worst" pinning

cycles per instruction [3]: LEC: avrora (uncontrolled) and luindex (controlled), "average" pinning

uncontrolled throughput [4]: LEC: avrora (uncontrolled) and luindex (controlled), "best" pinning

uncontrolled throughput [4]: LEC: avrora (uncontrolled) and luindex (controlled), "worst" pinning

controlled throughput [4]: LEC: avrora (uncontrolled) and luindex (controlled), "best" pinning

controlled throughput [4]: LEC: avrora (uncontrolled) and luindex (controlled), "worst" pinning

power consumption [5]: LEC: avrora (uncontrolled) and luindex (controlled), "best" pinning

power consumption [5]: LEC: avrora (uncontrolled) and luindex (controlled), "worst" pinning

power consumption [5]: LEC: avrora (uncontrolled) and luindex (controlled), "average" pinning
cycles per instruction [1] : LRC : avrora (uncontrolled) and specs (controlled), "best" pinning

uncontrolled throughput [1/4] : LRC : avrora (uncontrolled) and specs (controlled), "best" pinning

controlled throughput [1/4] : LRC : avrora (uncontrolled) and specs (controlled), "best" pinning

cycles per instruction [1] : LRC : avrora (uncontrolled) and specs (controlled), "worst" pinning

uncontrolled throughput [1/4] : LRC : avrora (uncontrolled) and specs (controlled), "worst" pinning

controlled throughput [1/4] : LRC : avrora (uncontrolled) and specs (controlled), "worst" pinning

power consumption [W] : LRC : avrora (uncontrolled) and specs (controlled), "best" pinning

uncontrolled throughput [1/4] : LRC : avrora (uncontrolled) and specs (controlled), "worst" pinning

controlled throughput [1/4] : LRC : avrora (uncontrolled) and specs (controlled), "worst" pinning

power consumption [W] : LRC : avrora (uncontrolled) and specs (controlled), "worst" pinning
cycles per instruction \([1]\) : L2C : h2 (uncontrolled) and huindex (controlled), "best" pinning

cycles per instruction \([1]\) : L2C : h2 (uncontrolled) and huindex (controlled), "worse" pinning

cycles per instruction \([1]\) : L2C : h2 (uncontrolled) and huindex (controlled), "worst" pinning

uncorrected throughput \([1]\) : L2C : h2 (uncontrolled) and huindex (controlled), "best" pinning

uncorrected throughput \([1]\) : L2C : h2 (uncontrolled) and huindex (controlled), "worse" pinning

uncorrected throughput \([1]\) : L2C : h2 (uncontrolled) and huindex (controlled), "worst" pinning

controlled throughput \([1]\) : L2C : h2 (uncontrolled) and huindex (controlled), "best" pinning

controlled throughput \([1]\) : L2C : h2 (uncontrolled) and huindex (controlled), "worse" pinning

controlled throughput \([1]\) : L2C : h2 (uncontrolled) and huindex (controlled), "worst" pinning

cycles per instruction \([1]\) : L2C : h2 (uncontrolled) and huindex (controlled), "best" pinning

cycles per instruction \([1]\) : L2C : h2 (uncontrolled) and huindex (controlled), "worse" pinning

cycles per instruction \([1]\) : L2C : h2 (uncontrolled) and huindex (controlled), "worst" pinning

power consumption \([1]\) : L2C : h2 (uncontrolled) and huindex (controlled), "best" pinning

power consumption \([1]\) : L2C : h2 (uncontrolled) and huindex (controlled), "worse" pinning

power consumption \([1]\) : L2C : h2 (uncontrolled) and huindex (controlled), "worst" pinning
cycles per instruction [1]: LXC : luindex (uncontrolled) and avrora (controlled), "best" pinning

cycles per instruction [2]: LXC : luindex (uncontrolled) and avrora (controlled), "worse" pinning

uncontrolled throughput [1]: LXC : luindex (uncontrolled) and avrora (controlled), "best" pinning

uncontrolled throughput [2]: LXC : luindex (uncontrolled) and avrora (controlled), "worse" pinning

controlled throughput [1]: LXC : luindex (uncontrolled) and avrora (controlled), "best" pinning

controlled throughput [2]: LXC : luindex (uncontrolled) and avrora (controlled), "worse" pinning

cycles per instruction [1]: LXC : luindex (uncontrolled) and avrora (controlled), "best" pinning

cycles per instruction [2]: LXC : luindex (uncontrolled) and avrora (controlled), "worse" pinning

power consumption [3]: LXC : luindex (uncontrolled) and avrora (controlled), "best" pinning

power consumption [4]: LXC : luindex (uncontrolled) and avrora (controlled), "worse" pinning
cycles per instruction [1] : LXC : luindex (uncontrolled) and luindex (controlled), "best" pinning

uncontrolled throughput [1/s] : LXC : luindex (uncontrolled) and luindex (controlled), "best" pinning

controlled throughput [1/s] : LXC : luindex (uncontrolled) and luindex (controlled), "best" pinning

cycles per instruction [1] : LXC : luindex (uncontrolled) and luindex (controlled), "worse" pinning

uncontrolled throughput [1/s] : LXC : luindex (uncontrolled) and luindex (controlled), "worse" pinning

controlled throughput [1/s] : LXC : luindex (uncontrolled) and luindex (controlled), "worse" pinning

power consumption [W] : LXC : luindex (uncontrolled) and luindex (controlled), "best" pinning

power consumption [W] : LXC : luindex (uncontrolled) and luindex (controlled), "worse" pinning
cycles per instruction (1) : LEC : space (uncontrolled) and space (controlled), "best" pinning

cycles per instruction (2) : LEC : space (uncontrolled) and space (controlled), "worse" pinning

cycles per instruction (3) : LEC : space (uncontrolled) and space (controlled), "worst" pinning

uncontrolled throughput (1x) : LEC : space (uncontrolled) and space (controlled), "best" pinning

uncontrolled throughput (2x) : LEC : space (uncontrolled) and space (controlled), "worse" pinning

uncontrolled throughput (3x) : LEC : space (uncontrolled) and space (controlled), "worst" pinning

controlled throughput (1x) : LEC : space (uncontrolled) and space (controlled), "best" pinning

controlled throughput (2x) : LEC : space (uncontrolled) and space (controlled), "worse" pinning

controlled throughput (3x) : LEC : space (uncontrolled) and space (controlled), "worst" pinning

cycles per instruction (4) : LEC : space (uncontrolled) and space (controlled), "best" pinning

cycles per instruction (5) : LEC : space (uncontrolled) and space (controlled), "worse" pinning

cycles per instruction (6) : LEC : space (uncontrolled) and space (controlled), "worst" pinning

power consumption (1x) : LEC : space (uncontrolled) and space (controlled), "best" pinning

power consumption (2x) : LEC : space (uncontrolled) and space (controlled), "worse" pinning

power consumption (3x) : LEC : space (uncontrolled) and space (controlled), "worst" pinning

L2 cache miss rate

L1 cache miss rate

Instruction execution time