cycles per instruction [1]: LXC : avrora (uncontrolled) and avrora (controlled), "best" pinning

cycles per instruction [1]: LXC : avrora (uncontrolled) and avrora (controlled), "worst" pinning

power consumption [W]: LXC : avrora (uncontrolled) and avrora (controlled), "best" pinning

power consumption [W]: LXC : avrora (uncontrolled) and avrora (controlled), "worst" pinning
cycles per instruction [1] : LXC : avrora (uncontrolled) and h2 (controlled), "best" pinning

cycles per instruction [1] : LXC : avrora (uncontrolled) and h2 (controlled), "worst" pinning

power consumption [W] : LXC : avrora (uncontrolled) and h2 (controlled), "best" pinning

power consumption [W] : LXC : avrora (uncontrolled) and h2 (controlled), "worst" pinning
cycles per instruction [1]: LXC : avrora (uncontrolled) and luindex (controlled), "best" pinning
power consumption [W]: LXC : avrora (uncontrolled) and luindex (controlled), "best" pinning

cycles per instruction [1]: LXC : avrora (uncontrolled) and luindex (controlled), "worst" pinning
power consumption [W]: LXC : avrora (uncontrolled) and luindex (controlled), "worst" pinning
cycles per instruction [\text{cycles per instruction}] : LXC : avrora (uncontrolled) and scalac (controlled), "best" pinning

cycles per instruction [\text{cycles per instruction}] : LXC : avrora (uncontrolled) and scalac (controlled), "worst" pinning

power consumption [\text{power consumption}] : LXC : avrora (uncontrolled) and scalac (controlled), "best" pinning

power consumption [\text{power consumption}] : LXC : avrora (uncontrolled) and scalac (controlled), "worst" pinning
cycles per instruction [1]: LXC : avrora (uncontrolled) and specs (controlled), "best" pinning

cycles per instruction [1]: LXC : avrora (uncontrolled) and specs (controlled), "worse" pinning

power consumption [W]: LXC : avrora (uncontrolled) and specs (controlled), "best" pinning

power consumption [W]: LXC : avrora (uncontrolled) and specs (controlled), "worse" pinning
cycles per instruction [1] : LXC : h2 (uncontrolled) and avrora (controlled), "best" pinning

cycles per instruction [1] : LXC : h2 (uncontrolled) and avrora (controlled), "worst" pinning

power consumption [W] : LXC : h2 (uncontrolled) and avrora (controlled), "best" pinning

power consumption [W] : LXC : h2 (uncontrolled) and avrora (controlled), "worst" pinning
cycles per instruction [1] : LXC : h2 (uncontrolled) and h2 (controlled), "best" pinning

cycles per instruction [1] : LXC : h2 (uncontrolled) and h2 (controlled), "worse" pinning

cycles per instruction [1] : LXC : h2 (uncontrolled) and h2 (controlled), "worst" pinning

power consumption [W] : LXC : h2 (uncontrolled) and h2 (controlled), "best" pinning

power consumption [W] : LXC : h2 (uncontrolled) and h2 (controlled), "worse" pinning

power consumption [W] : LXC : h2 (uncontrolled) and h2 (controlled), "worst" pinning
cycles per instruction [1] : LXC : h2 (uncontrolled) and luindex (controlled), "best" pinning

cycles per instruction [1] : LXC : h2 (uncontrolled) and luindex (controlled), "worse" pinning

power consumption [W] : LXC : h2 (uncontrolled) and luindex (controlled), "best" pinning

power consumption [W] : LXC : h2 (uncontrolled) and luindex (controlled), "worse" pinning
cycles per instruction [5] : H2 (uncontrolled) and scalac (controlled), "best" pinning

cycles per instruction [5] : H2 (uncontrolled) and scalac (controlled), "worst" pinning

power consumption [W] : H2 (uncontrolled) and scalac (controlled), "best" pinning

power consumption [W] : H2 (uncontrolled) and scalac (controlled), "worst" pinning
cycles per instruction \([1]\) : LXC : h2 (uncontrolled) and specs (controlled), "best" pinning

cycles per instruction \([1]\) : LXC : h2 (uncontrolled) and specs (controlled), "worse" pinning

cycles per instruction \([1]\) : LXC : h2 (uncontrolled) and specs (controlled), "worst" pinning

power consumption \([W]\) : LXC : h2 (uncontrolled) and specs (controlled), "best" pinning

power consumption \([W]\) : LXC : h2 (uncontrolled) and specs (controlled), "worse" pinning

power consumption \([W]\) : LXC : h2 (uncontrolled) and specs (controlled), "worst" pinning
cycles per instruction [1] : LXC : luindex (uncontrolled) and specs (controlled), "best" pinning

cycles per instruction [1] : LXC : luindex (uncontrolled) and specs (controlled), "worst" pinning

power consumption [W] : LXC : luindex (uncontrolled) and specs (controlled), "best" pinning

power consumption [W] : LXC : luindex (uncontrolled) and specs (controlled), "worst" pinning
cycles per instruction [1]: LXC: scalac (uncontrolled) and avrora (controlled), "best" pinning

cycles per instruction [1]: LXC: scalac (uncontrolled) and avrora (controlled), "worst" pinning

power consumption [W]: LXC: scalac (uncontrolled) and avrora (controlled), "best" pinning

power consumption [W]: LXC: scalac (uncontrolled) and avrora (controlled), "worst" pinning
cycles per instruction [1]: LXC: scalac (uncontrolled) and h2 (controlled), "best" pinning

cycles per instruction [1]: LXC: scalac (uncontrolled) and h2 (controlled), "worst" pinning

power consumption [W]: LXC: scalac (uncontrolled) and h2 (controlled), "best" pinning

power consumption [W]: LXC: scalac (uncontrolled) and h2 (controlled), "worst" pinning
cycles per instruction [1] : LKC : scalac (uncontrolled) and scalac (controlled), "best" pinning

cycles per instruction [1] : LKC : scalac (uncontrolled) and scalac (controlled), "worst" pinning

power consumption [W] : LKC : scalac (uncontrolled) and scalac (controlled), "best" pinning

power consumption [W] : LKC : scalac (uncontrolled) and scalac (controlled), "worst" pinning
cycles per instruction [T] : LXC : scalac (uncontrolled) and specs (controlled), "best" pinning

cycles per instruction [T] : LXC : scalac (uncontrolled) and specs (controlled), "worse" pinning

power consumption [W] : LXC : scalac (uncontrolled) and specs (controlled), "best" pinning

power consumption [W] : LXC : scalac (uncontrolled) and specs (controlled), "worse" pinning
cycles per instruction [T] : LXC : specs (uncontrolled) and h2 (controlled), "best" pinning

cycles per instruction [T] : LXC : specs (uncontrolled) and h2 (controlled), "worse" pinning

cycles per instruction [T] : LXC : specs (uncontrolled) and h2 (controlled), "worst" pinning

power consumption [W] : LXC : specs (uncontrolled) and h2 (controlled), "best" pinning

power consumption [W] : LXC : specs (uncontrolled) and h2 (controlled), "worse" pinning

power consumption [W] : LXC : specs (uncontrolled) and h2 (controlled), "worst" pinning
cycles per instruction [1]: LXC : specs (uncontrolled) and luindex (controlled), "best" pinning

cycles per instruction [1]: LXC : specs (uncontrolled) and luindex (controlled), "worse" pinning

cycles per instruction [1]: LXC : specs (uncontrolled) and luindex (controlled), "worse" pinning

power consumption [W]: LXC : specs (uncontrolled) and luindex (controlled), "best" pinning

power consumption [W]: LXC : specs (uncontrolled) and luindex (controlled), "worse" pinning

power consumption [W]: LXC : specs (uncontrolled) and luindex (controlled), "worse" pinning