cycles per instruction [1] : LXC : avrora (uncontrolled) and avrora (controlled), "best" pinning

cycles per instruction [1] : LXC : avrora (uncontrolled) and avrora (controlled), "worst" pinning

uncontrolled throughput [1/s] : LXC : avrora (uncontrolled) and avrora (controlled), "best" pinning

uncontrolled throughput [1/s] : LXC : avrora (uncontrolled) and avrora (controlled), "worst" pinning

controlled throughput [1/s] : LXC : avrora (uncontrolled) and avrora (controlled), "best" pinning

controlled throughput [1/s] : LXC : avrora (uncontrolled) and avrora (controlled), "worst" pinning
cycles per instruction [1] : avrora (uncontrolled) and h2 (controlled), "best" pinning

cycles per instruction [1] : avrora (uncontrolled) and h2 (controlled), "worst" pinning

uncontrolled throughput [1/s] : avrora (uncontrolled) and h2 (controlled), "best" pinning

uncontrolled throughput [1/s] : avrora (uncontrolled) and h2 (controlled), "worst" pinning

controlled throughput [1/s] : avrora (uncontrolled) and h2 (controlled), "best" pinning

controlled throughput [1/s] : avrora (uncontrolled) and h2 (controlled), "worst" pinning
cycles per instruction [1]: LXC : avrora (uncontrolled) and scalac (controlled), "best" pinning

uncontrolled throughput [1/a]: LXC : avrora (uncontrolled) and scalac (controlled), "best" pinning

cycles per instruction [1]: LXC : avrora (uncontrolled) and scalac (controlled), "worst" pinning

uncontrolled throughput [1/a]: LXC : avrora (uncontrolled) and scalac (controlled), "worst" pinning

cycles per instruction [1]: LXC : avrora (uncontrolled) and scalac (controlled), "worst" pinning

controlled throughput [1/a]: LXC : avrora (uncontrolled) and scalac (controlled), "best" pinning

uncontrolled throughput [1/a]: LXC : avrora (uncontrolled) and scalac (controlled), "worst" pinning

cycles per instruction [1]: LXC : avrora (uncontrolled) and scalac (controlled), "worst" pinning

controlled throughput [1/a]: LXC : avrora (uncontrolled) and scalac (controlled), "worst" pinning
cycles per instruction [1]: LXC : h2 (uncontrolled) and avrora (controlled), "best" pinning

cycles per instruction [1]: LXC : h2 (uncontrolled) and avrora (controlled), "worse" pinning

uncontrolled throughput [1/1]: LXC : h2 (uncontrolled) and avrora (controlled), "best" pinning

uncontrolled throughput [1/1]: LXC : h2 (uncontrolled) and avrora (controlled), "worse" pinning

controlled throughput [1/1]: LXC : h2 (uncontrolled) and avrora (controlled), "best" pinning

controlled throughput [1/1]: LXC : h2 (uncontrolled) and avrora (controlled), "worse" pinning
cycles per instruction [1]: LXC : h2 (uncontrolled) and h2 (controlled), "best" pinning

cycles per instruction [1]: LXC : h2 (uncontrolled) and h2 (controlled), "worst" pinning

uncontrolled throughput [1]: LXC : h2 (uncontrolled) and h2 (controlled), "best" pinning

uncontrolled throughput [1]: LXC : h2 (uncontrolled) and h2 (controlled), "worst" pinning

controlled throughput [1]: LXC : h2 (uncontrolled) and h2 (controlled), "best" pinning

controlled throughput [1]: LXC : h2 (uncontrolled) and h2 (controlled), "worst" pinning

controlled throughput [1]: LXC : h2 (uncontrolled) and h2 (controlled), "best" pinning

controlled throughput [1]: LXC : h2 (uncontrolled) and h2 (controlled), "worst" pinning
cycles per instruction: LXC : h2 (uncontrolled) and luindex (controlled), "best" pinning

cycles per instruction: LXC : h2 (uncontrolled) and luindex (controlled), "worst" pinning

uncontrolled throughput: LXC : h2 (uncontrolled) and luindex (controlled), "best" pinning

uncontrolled throughput: LXC : h2 (uncontrolled) and luindex (controlled), "worst" pinning

controlled throughput: LXC : h2 (uncontrolled) and luindex (controlled), "best" pinning

controlled throughput: LXC : h2 (uncontrolled) and luindex (controlled), "worst" pinning
cycles per instruction [1/s]: LXC : h2 (uncontrolled) and specs (controlled), "best" pinning

uncontrolled throughput [1/s]: LXC : h2 (uncontrolled) and specs (controlled), "best" pinning

controlled throughput [1/s]: LXC : h2 (uncontrolled) and specs (controlled), "best" pinning

cycles per instruction [1/s]: LXC : h2 (uncontrolled) and specs (controlled), "worse" pinning

uncontrolled throughput [1/s]: LXC : h2 (uncontrolled) and specs (controlled), "worse" pinning

controlled throughput [1/s]: LXC : h2 (uncontrolled) and specs (controlled), "worse" pinning
cycles per instruction (T) : LIXC : luidex (uncontrolled) and h2 (controlled), "best" pinning

cycles per instruction (T) : LIXC : luidex (uncontrolled) and h2 (controlled), "worse" pinning

uncontrolled throughput (T/a) : LIXC : luidex (uncontrolled) and h2 (controlled), "best" pinning

uncontrolled throughput (T/a) : LIXC : luidex (uncontrolled) and h2 (controlled), "worse" pinning

controlled throughput (T/a) : LIXC : luidex (uncontrolled) and h2 (controlled), "best" pinning

controlled throughput (T/a) : LIXC : luidex (uncontrolled) and h2 (controlled), "worse" pinning
cycles per instruction [1]: LXC: scalac (uncontrolled) and h2 (controlled), "best" pinning

cycles per instruction [1]: LXC: scalac (uncontrolled) and h2 (controlled), "worst" pinning

uncontrolled throughput [1/s]: LXC: scalac (uncontrolled) and h2 (controlled), "best" pinning

uncontrolled throughput [1/s]: LXC: scalac (uncontrolled) and h2 (controlled), "worst" pinning

controlled throughput [1/s]: LXC: scalac (uncontrolled) and h2 (controlled), "best" pinning

controlled throughput [1/s]: LXC: scalac (uncontrolled) and h2 (controlled), "worst" pinning
cycles per instruction (1): LXC : scalac (uncontrolled) and scalac (controlled), "best" pinning

cycles per instruction (1): LXC : scalac (uncontrolled) and scalac (controlled), "worse" pinning

uncontrolled throughput [1/s]: LXC : scalac (uncontrolled) and scalac (controlled), "best" pinning

uncontrolled throughput [1/s]: LXC : scalac (uncontrolled) and scalac (controlled), "worse" pinning

controlled throughput [1/s]: LXC : scalac (uncontrolled) and scalac (controlled), "best" pinning

controlled throughput [1/s]: LXC : scalac (uncontrolled) and scalac (controlled), "worse" pinning
cycles per instruction [1]: LXC : specs (uncontrolled) and h2 (controlled), "best" pinning

cycles per instruction [1]: LXC : specs (uncontrolled) and h2 (controlled), "worse" pinning

cycles per instruction [1]: LXC : specs (uncontrolled) and h2 (controlled), "worst" pinning

uncontrolled throughput [1/s]: LXC : specs (uncontrolled) and h2 (controlled), "best" pinning

uncontrolled throughput [1/s]: LXC : specs (uncontrolled) and h2 (controlled), "worse" pinning

uncontrolled throughput [1/s]: LXC : specs (uncontrolled) and h2 (controlled), "worst" pinning

controlled throughput [1/s]: LXC : specs (uncontrolled) and h2 (controlled), "best" pinning

controlled throughput [1/s]: LXC : specs (uncontrolled) and h2 (controlled), "worse" pinning

controlled throughput [1/s]: LXC : specs (uncontrolled) and h2 (controlled), "worst" pinning
cycles per instruction [1]: LXC: specs (uncontrolled) and specs (controlled), "best" pinning

uncontrolled throughput [1/a]: LXC: specs (uncontrolled) and specs (controlled), "best" pinning

cycles per instruction [1]: LXC: specs (uncontrolled) and specs (controlled), "worse" pinning

uncontrolled throughput [1/a]: LXC: specs (uncontrolled) and specs (controlled), "worse" pinning

controlled throughput [1/a]: LXC: specs (uncontrolled) and specs (controlled), "best" pinning

uncontrolled throughput [1/a]: LXC: specs (uncontrolled) and specs (controlled), "worst" pinning

cycles per instruction [1]: LXC: specs (uncontrolled) and specs (controlled), "worst" pinning

controlled throughput [1/a]: LXC: specs (uncontrolled) and specs (controlled), "worst" pinning