cycles per instruction [1]: KVM: luindex (uncontrolled) and luindex (controlled), "neutral" pinning

node load|store|prefetch [6/s]: KVM: luindex (uncontrolled) and luindex (controlled), "worst" pinning
cycles per instruction \[1\] : KVM : specs (uncontrolled) and specs (controlled), "neutral" pinning

node l|s|p miss rate \[1\] : KVM : specs (uncontrolled) and specs (controlled), "worse" pinning