# NSWli01: System Behaviour Models and Verification 

## 9. ABSTRACTIONS AND SYMMETRIES

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## TODAY

- Abstractions
- Symmetries and Partial order reduction


## Part I: Abstractions

## AbSTRACTIONS



## Abstractions



AG (start $\rightarrow$ AF heat)


Model Checker

Property satisfied

Property violated

Property specification

## AbSTRACTIONS

Ways to reduce (simplify) state space - optimizations - realized as elimination of some details of model

Particular abstractions:

- Cone of influence reduction
- Data abstraction


## Cone of Influence Reduction (COIR)

- Focus just on variables related to specification, i.e., those in formula to be model checked
- Variables not influencing values of variables in specification can be removed they cannot affect whether the spec is valid or not


## CoIR - Definition

- Let $S$ be synchronous circuit described by set of equations $v_{i}=f_{i}(V)$
- $v_{i} \in V$
- $f_{i}$ are Boolean functions
- Let specification contain set of variables $V^{\prime} \subseteq V$
- Some $x \in V^{\prime}$ can depend on $y \notin V^{\prime}$
- We define set $C \supseteq V^{\prime}$ of interest $-C$ as cone


## CoIR - Definition

The cone of influence $C$ of $V^{\prime}$ is the minimal set of variables such that:

- $V^{\prime} \subseteq C$, and
- $\forall \mathrm{k}, j:\left(v_{k} \in C\right) \wedge\left(f_{k}\right.$ depends on $\left.v_{j}\right) \Longrightarrow v_{j} \in C$

New (reduced) system is constructed from original by removing all equations whose left-hand-side variables do not appear in C

## Correctness of Reduction

Theorem: Let $f$ be CTL formula and $M$ Kripke structure. Let $M^{\prime}$ be Kripke structure after CoIR of $M$ with respect to $f$. Then $M \models f \Leftrightarrow M^{\prime} \models f$.

Proof idea: Removing variables not in C and adding transitions existing in original states with identical values.

## COIR - EXAMPLE

Specification comprising three variables:

- $v_{0}^{\prime}=\neg v_{0}$
- $v_{1}^{\prime}=v_{0} \oplus v_{1}$
- $v_{2}^{\prime}=\left(v_{0} \wedge v_{1}\right) \oplus v_{2}$

Corresponding Kripke structure $M=(S, I, R, L)$ over variables $V=\left\{v_{1}, \ldots, v_{n}\right\}$ :

- $S=\{0,1\}^{n}$
- $I \subseteq S$
- $R=\bigwedge_{i=1}^{n}\left[v_{i}^{\prime}=f_{i}(V)\right]$
- $L(s)=\left\{v_{i} \mid s\left(v_{i}\right)=1,1 \leq i \leq n\right\}$


## Original State Space



## Reduced Model

Let $C=\left\{v_{1}, . . v_{k}\right\}$ for some $k$ be Col. Reduced model is $M^{\prime}=\left(S^{\prime}, I^{\prime}, R^{\prime}, L^{\prime}\right)$ :

- $S^{\prime}=\{0,1\}^{k}$
- $I^{\prime}=\left\{\left(d_{1}^{\prime}, . . d_{k}^{\prime}\right) \mid \exists\left(d_{1}, ., d_{n}\right) \in I: d_{i}^{\prime}=d_{i}, 1 \leq i \leq k\right\}$
- $R^{\prime}=\bigcap_{i=1}^{k}\left[v_{i}^{\prime}=f_{i}(V)\right]$
- $L^{\prime}\left(s^{\prime}\right)=\left\{v_{i} \mid s^{\prime}\left(v_{i}\right)=1,1 \leq i \leq k\right\}$


## Reduced State Space

$$
V^{\prime}=\left\{v_{0}\right\} \rightarrow C=\left\{v_{0}\right\}
$$



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$V^{\prime}=\left\{v_{2}\right\} \rightarrow C=\left\{v_{0}, v_{1}, v_{2}\right\}$ - original state space

## ВАск to Proof Sкетсн

Let $B \subseteq S \times S^{\prime}$ be defined as follows: $\left(\left(d_{1}, . ., d_{n}\right),\left(d_{1}^{\prime}, . ., d_{k}^{\prime}\right)\right) \in B \Leftrightarrow d_{i}=d_{i}^{\prime}, \forall 1 \leq i \leq k$ It suffices to show that $B$ is bisimulation

- bisimulation implies $M \models f \Leftrightarrow M^{\prime} \models f$
- first consider initial states and then all transitions and target states
- it is easy to see that it is bisimulation ;-)


## Data Abstraction - Motivation

- Number of combinations of possible values of (user) input can be enormous
- Results in very large or sometimes even infinite (floating point numbers) state space
- Model checking hard or not possible in principle
- Solution: Data abstraction


## Data Abstraction - Procedure

1. Define abstract domain(s) and map concrete values to abstract ones
2. Create reduced Kripke structure

### 2.1 Replace concrete AP with abstract AP

2.2 Merge states with same set of AP
3. Model checking

## 1. Abstract Domains

- Motivation is to significantly lower number of possible values for selected variables
- Abstraction means hiding some information
- Done by mapping each concrete value to abstract one, e.g., integer domain can be mapped to abstract domain
- Note that in Kripke structure data are encoded in (Boolean) atomic propositions


## Data Abstraction - Example

Let $A=\left\{a_{0}, a_{+}, a_{-}\right\}$be abstract domain and $h(x)$ mapping (abstraction) function
For int $i: h(i)=a_{0}$ if $i=0$

$$
\begin{aligned}
& h(i)=a_{+} \text {if } i>0 \\
& h(i)=a_{-} \text {if } i<0
\end{aligned}
$$

Corresponding atomic propositions for concrete variables:

$$
h(x)=a_{0}, h(x)=a_{+}, h(x)=a_{-}
$$

## 2. Creating Reduced Kripke Structure

1. Create $M^{\prime}=\left(S, I, R, L^{\prime}\right)$ such that it is identical to $M$ except for $L-L^{\prime}$ labels states with abstract atomic propositions
2. Create reduced Kripke structure $M_{r}=\left(S_{r}, I_{r}, R_{r}, L_{r}\right)$ :

- $S_{r}=\left\{L^{\prime}(s) \mid s \in S\right\}$ - merging states with identical set of AP
- $s_{r} \in I_{r} \Leftrightarrow \exists s \in S: s_{r}=L^{\prime}(s) \wedge s \in I$
- $\left(s_{r}, t_{r}\right) \in R_{r} \Leftrightarrow \exists s, t \in S:(s, t) \in R \wedge s_{r}=L^{\prime}(s) \wedge t_{r}=L^{\prime}(t)$


## 3. Model Checking

Perform model checking of $M_{r}$
Desired property: $M_{r} \models f_{r} \Longrightarrow M \models f$. Does this hold for any CTL formula?

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> Example: Traffic lights


## Data Abstraction - Example

Original atomic propositions $A P=\{$ red, yellow, green $\}$, in each state exactly one is true Abstract domain $A=\{$ stop, go $\}$

Mapping function $h$ : $h($ red $)=$ stop, $h($ yellow $)=$ stop, $h($ green $)=$ go


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## Our Property

Desired property: $M_{r} \models f_{r} \Longrightarrow M \models f$. Does this hold for any CTL formula? NO:
Consider formula AG (red $\wedge$ EX yellow) which is not satisfied in red state.
After abstraction formula reads AG (stop $\wedge \mathrm{EX}$ stop) and this formula is satisfied in stop state of $M_{r}$.

## So what is it good for?!?

## Data Abstraction - JUstification

We cannot do arbitrary abstractions - this way we could reduce any KS into one with just one state and possibly self loop, which is apparently not correct.

## Data Abstraction - Justification

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2. Limit language of formulae

- using ACTL - formulae in negative normal form without existential quantification


## Part II: Symmetries and Partial Order Reduction

## Symmetries

Concurrent systems often exhibit a lot of symmetry:

- memories
- caches
- buses

Identification of symmetric states can lead to substantial reduction of states space

- by checking just one representative of each symmetry group

We need to define what symmetric means in particular context/example

- property specification
- system model


## Symmetries

- From each set of symmetric states just one is picked, and its transitions are taken into account
- Formally, reduction is based on finding quotiens and invariant groups of automorphisms upon permutations
- skipping algebraic theory here for sake of time :-)


## Example - Token Ring Network

Token ring of two nodes


Reduced model based on symmetry:

$$
\{(t, n),(n, t)\} \text { and }\{(c, n),(n, c)\}
$$



## Example - Token Ring Network

We can apply reduction to larger configurations of $i$ nodes

- obtaining again two symmetry groups: $\left\{\left(t, n^{i}\right),\left(n, t, n^{i-1}\right), \ldots,\left(n^{i}, t\right)\right\}$ and $\left\{\left(c, n^{i}\right),\left(n, c, n^{i-1}\right), \ldots,\left(n^{i}, c\right)\right\}$

This results to exactly the same state space


## Execution Symmetry: Partial Order Reduction



Parallel composition (of processes) causes exponential growth of state space If processes do not communicate too much, majority of states are equivalent to other states and do not need to be explored

This means exploring just some paths from initial to final state


## Partial Order Reduction

Idea:

- Before model checking, reduced state graph is constructed
- Full state graph is never constructed
- Exploiting commutativity of concurrently executed transitions, which result in the same state when executed in different orders
- Formulated by Doron Peled in 1993

The name - Partial Order Reduction:

- Early versions were based on the partial order model of the program execution
- Better name: Model checking using representatives


## State Transition System

It is convenient to formulate the algorithm upon STS rather than Kripke structure

- Kripke structure $M=(S, I, R, L)$
- Corresponding STS $N=\left(S, T, S_{0}, L\right)$ :
- $S, S_{0}=I, L$ - identical to Kripke structure
- $T$ is set of transitions: $R\left(s, s^{\prime}\right) \Leftrightarrow \exists a \in T: a\left(s, s^{\prime}\right)$
- Transitions are labelled
- Transitions with the same label are considered the same transition


## Transitions

- Transition $a$ is enabled in state $s \Leftrightarrow \exists s^{\prime}: a\left(s, s^{\prime}\right)$
- If transition is not enabled, it is disabled
- enabled(s) refers to all enabled transition is state s
- Transition $a$ is deterministic
$\Leftrightarrow \forall s: a$ is enabled in $s$, there exists at most one state $s^{\prime}: a\left(s, s^{\prime}\right)$
- We can write $s^{\prime}=a(s)$
- Only deterministic systems will be considered


## Reduction Algorithm

```
expand_state(so)
function expand_state(s) {
    work_set = ample(s);
    while work_set is not empty {
    choose a from work_set
    work_set = work_set \ {a}
    t = a(s)
    if new(t)
        expand_state(t)
    create_edge(s, a, t);
    }
}
```


## Ample Set: Requirements

Systematic way of computing ample sets required
Desired properties of function ample(s):

1. Sufficiently many behaviours must be present in reduced state graph, so that algorithm provides correct results
2. Reduced state graph should be significantly smaller than full graph
3. Overhead of computing ample(s) must be reasonably small

Important notions are independence and invisibility of transitions

## Independence

Definition: Independence relation $I \subseteq T \times T$ is symmetric, anti-reflexive relation satisfying following two conditions:

- enabledness: $a, b \in \operatorname{enabled}(s) \Longrightarrow a \in \operatorname{enabled}(b(s))$
- commutativity: $a, b \in \operatorname{enabled}(s) \Longrightarrow a(b(s))=b(a(s))$

Definition exploits symmetry of relation
Dependency relation $D$ is complement of independence relation I: $D=(T \times T) \backslash I$ Independence relation to be specified:

- obtained either from computational model
- or knowledge of modelled system

Even actions that cannot be executed in parallel, e.g., incrementing variable by several processes, can be independent

## Independence in Spin

Let $a, b$ be transitions performed by different processes. $a, b$ are independent if:

- $a$ accesses local variable of its process, $b$ is arbitrary transition
- $a, b$ access two different global variables or channels
- Also including sending and receiving messages on different channels, and testing length of different channels
- $a, b$ read one global variable (or test length of one channel)
- $a$ is send operation on channel chan, $b$ is receive operation on chan, provided that chan is asynchronous and default behaviour of send is used (i.e., send on full channel is blocked)


## Invisibility And Stuttering Equivalence

Definition: Transition is called invisible if both origin and target states satisfy same set of atomic propositions.

- can be restricted to subset of AP
- invisible $\sim$ no visible change after executing the transition

Each path can be split into blocks, where each block contains states satisfying the same set of AP

Definition: Two paths are stuttering equivalent iff they contain the same blocks (w.r.t. AP) in the same order, possibly differing just in lengths.

- minimal length of each block is one
- block length is always finite


## Stuttering Equivalence of Structures

Two structures $M, M^{\prime}$ (Kripke structures or state transition systems) are stuttering equivalent iff:

- $M$ and $M^{\prime}$ have the same set of initial states
- for each path $\sigma$ of $M$ that starts from initial state $s$ of $M$ there exists path $\sigma^{\prime}$ of $M^{\prime}$ from the same initial state s such that $\sigma \sim_{\text {st }} \sigma^{\prime}$
- for each path $\sigma^{\prime}$ of $M^{\prime}$ that starts from initial state $s$ of $M^{\prime}$ there exists path $\sigma$ of $M$ from the same initial state $s$ such that $\sigma^{\prime} \sim_{s t} \sigma$


## Stuttering Equivalence in LTL

LTL formula is invariant under stuttering iff for each pair of paths $\pi$ and $\pi^{\prime}$ such that $\pi \sim_{\text {st }} \pi^{\prime}: \pi \models f \Leftrightarrow \pi^{\prime} \models f$.
We denote LTL without next operator by LTL-X
Theorem: Any LTL $_{-x}$ property is invariant under stuttering.
Theorem: Every LTL property that is invariant under stuttering can be expressed in LTL-x.
Theorem: Let $M, M^{\prime}$ be two stuttering equivalent structures. Then, for every LTL $-x$ property $f$, and every initial state $s: M, s \models f \Leftrightarrow M^{\prime}, s \models f$.

Idea: Partial Order Reduction generates stuttering equivalent structure and model-checks just this smaller structure

## Partial Order Reduction for LTL $-x$

Independence and invisibility are not enough, reduction has to address cycles and postponing transitions forever

State $s$ is fully expanded iff ample(s) = enabled(s)
Four conditions to be satisfied by ample(s) function:
Co. $\operatorname{ample}(s)=\emptyset \Leftrightarrow \operatorname{enabled}(s)=\emptyset$
C1. Along every path in full state graph that starts at $s$ it holds that transition dependent on transition in ample(s) cannot be executed without transition in ample(s) occurring first
C2. If $s$ is not fully expanded, then every $a \in$ ample( $s$ ) is invisible
C3. Cycle is not allowed if it contains state in which some transition $a$ is enabled, but is never included in ample(s) for any state s of the cycle

## POR in Java PathFinder

Java PathFinder is explicit code model checker for Java programs In principle special virtual machine executing "all" possible thread interleavings and "trying" all specified (random) input values

Since there are exponentially (in size and number of threads) many thread interleavings, switch only when it makes sense:

- For example, sequential update of local variables cannot affect other threads
- Consider just interesting instruction as re-scheduling points:
- scheduling-relevant instructions
- non-deterministic instructions


## POR in Java PathFinder

Only about 10\% are scheduling-relevant instructions:

- synchronization (monitorEnter, monitorExit, invokeX on synchronized methods)
- field access (putX, getX)
- array element access (Xaload, Xastore)
- thread instructions (start, sleep, yield, join)
- object methods (wait, notify)

