

How Good Are Your Invariants: Witness Validation for Hardware via Circuit Instrumentation with Software Invariants

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**Critical Systems
Research Group**

Introduction

Software and Hardware
Verification?

Motivation

Motivation

Word-level model
checking format
for hardware

Btor2

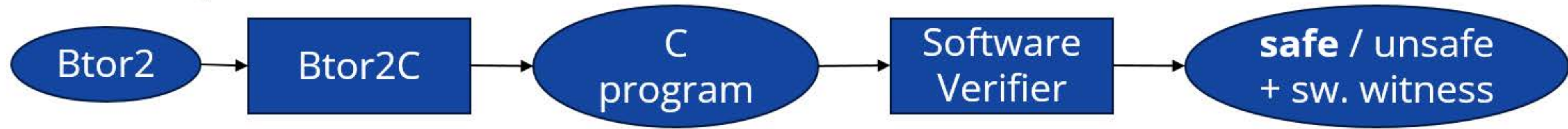
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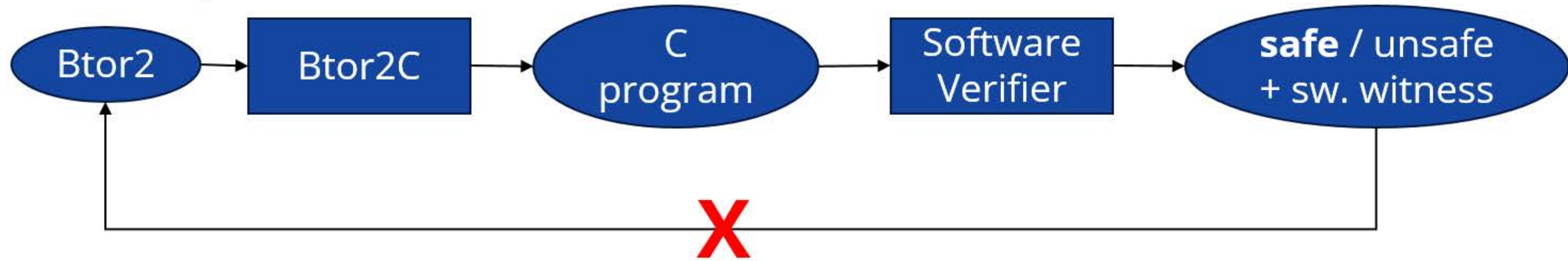
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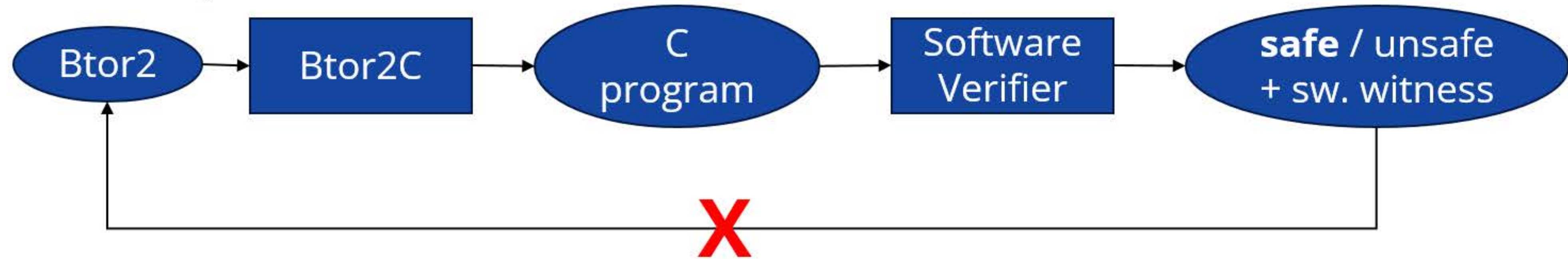
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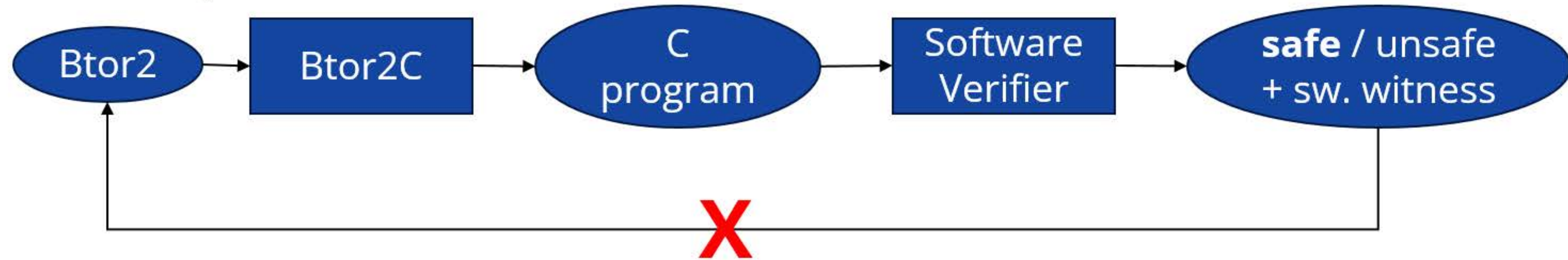
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Validation of Software Correctness Witnesses for Btor2C

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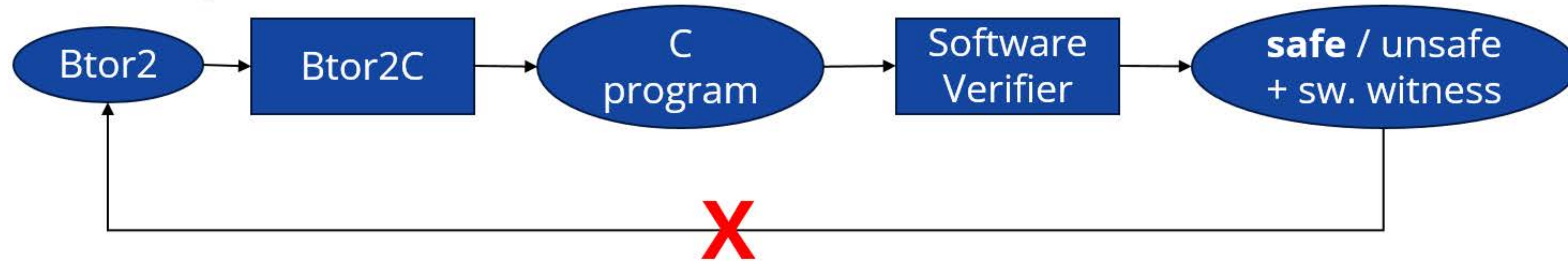


Validation of Software Correctness Witnesses for Btor2C

- Validate **software correctness witnesses** (invariants)
 - and provide insight on the "hardware side"

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Validation of Software Correctness Witnesses for Btor2C

- Validate **software correctness witnesses** (invariants)
 - and provide insight on the "hardware side"
- Show that there is **no discrepancy** inbetween C program and circuit
 - or find the issues

Validation Approach

From software back to Hardware

Challenges and Approach

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- No Btor2 correctness witness format

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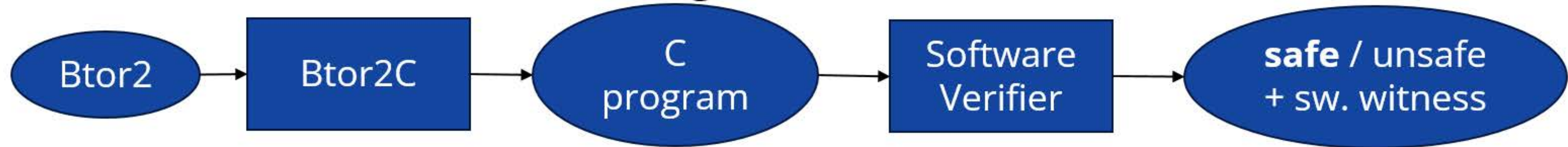
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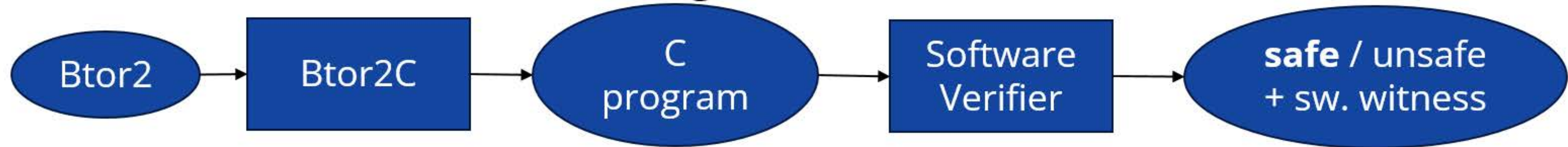
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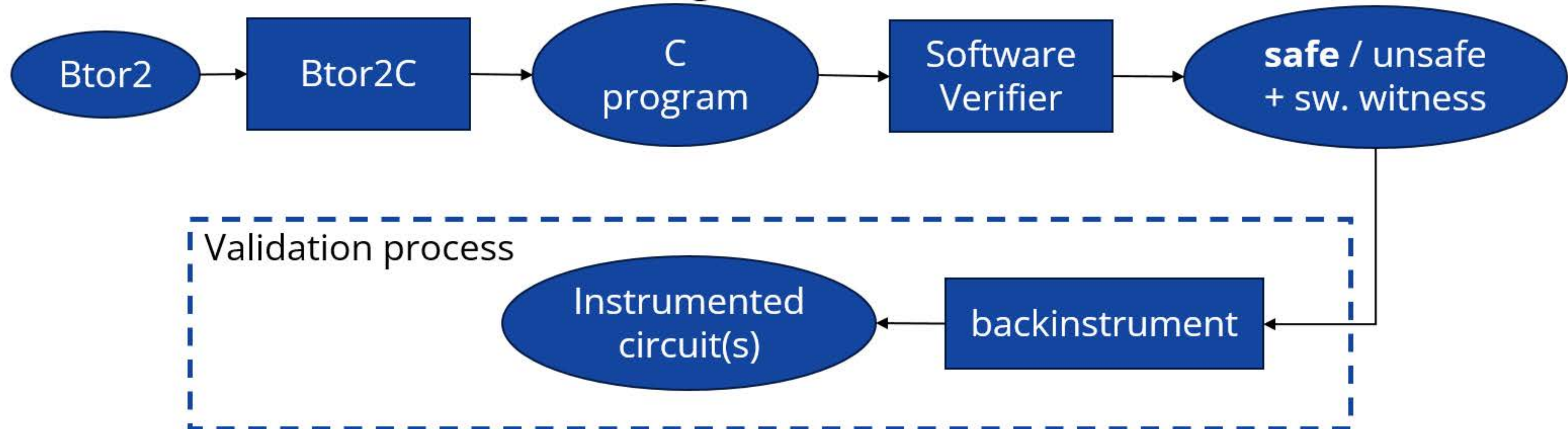
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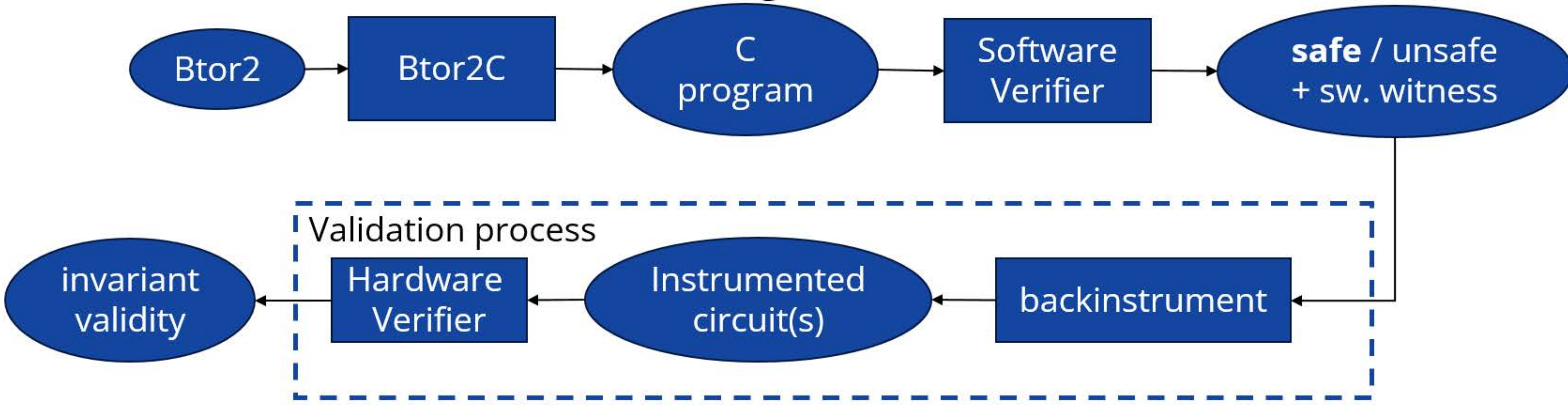
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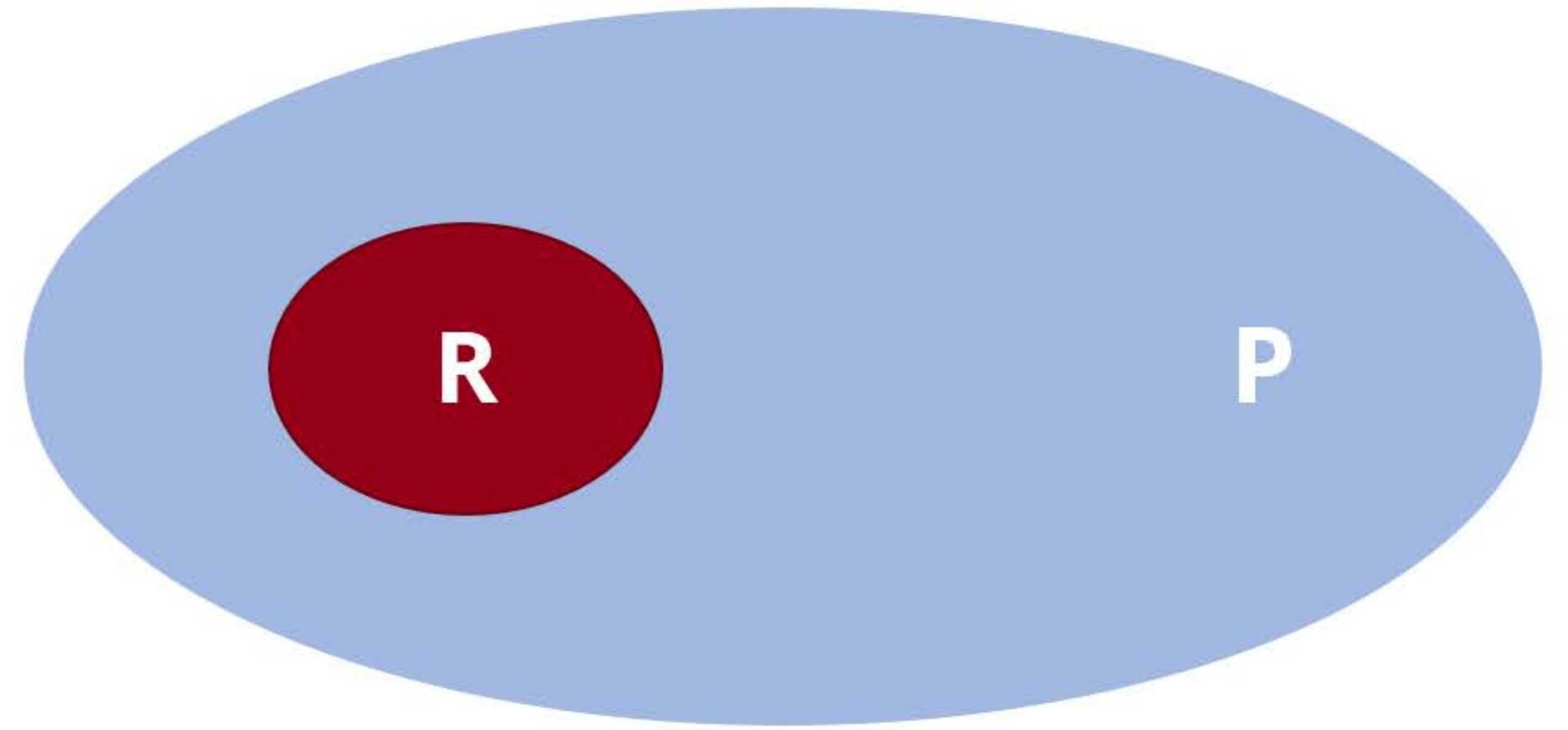


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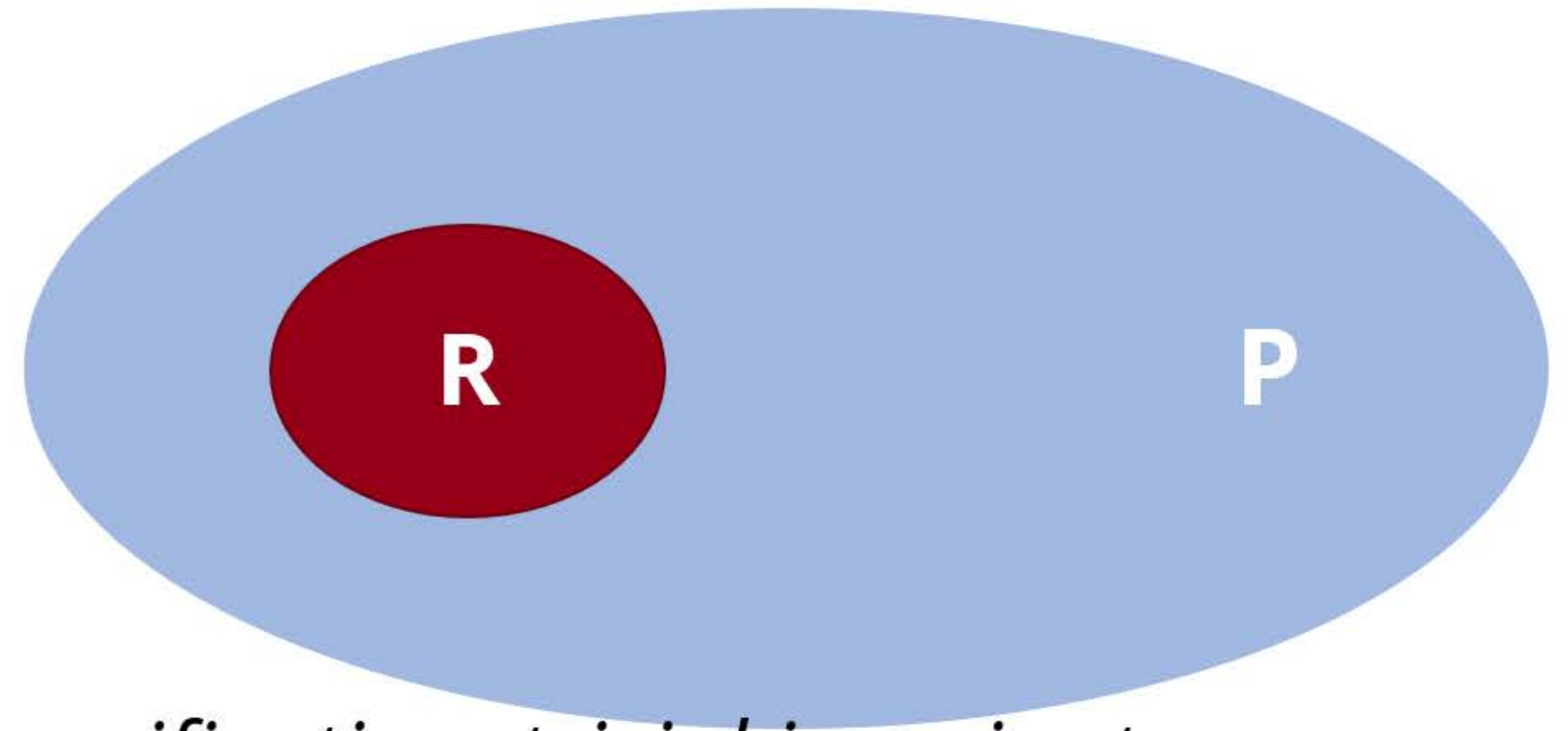
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How good is your invariant?

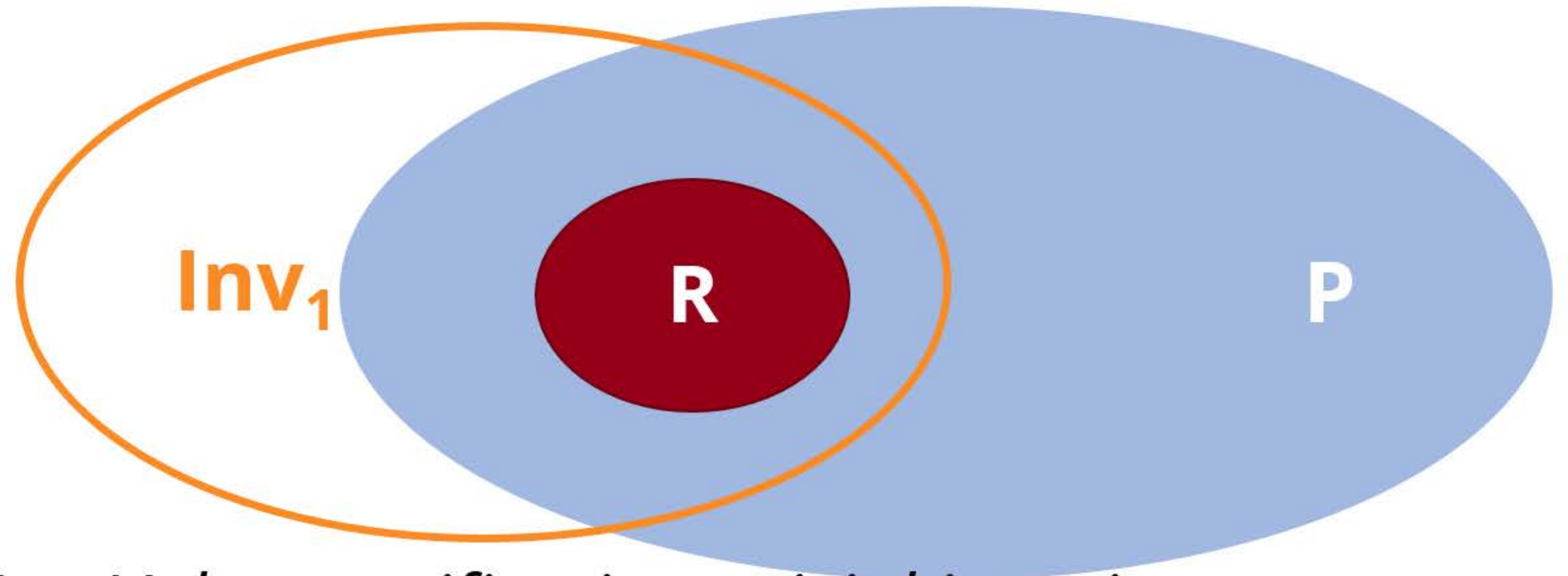


How good is your invariant?



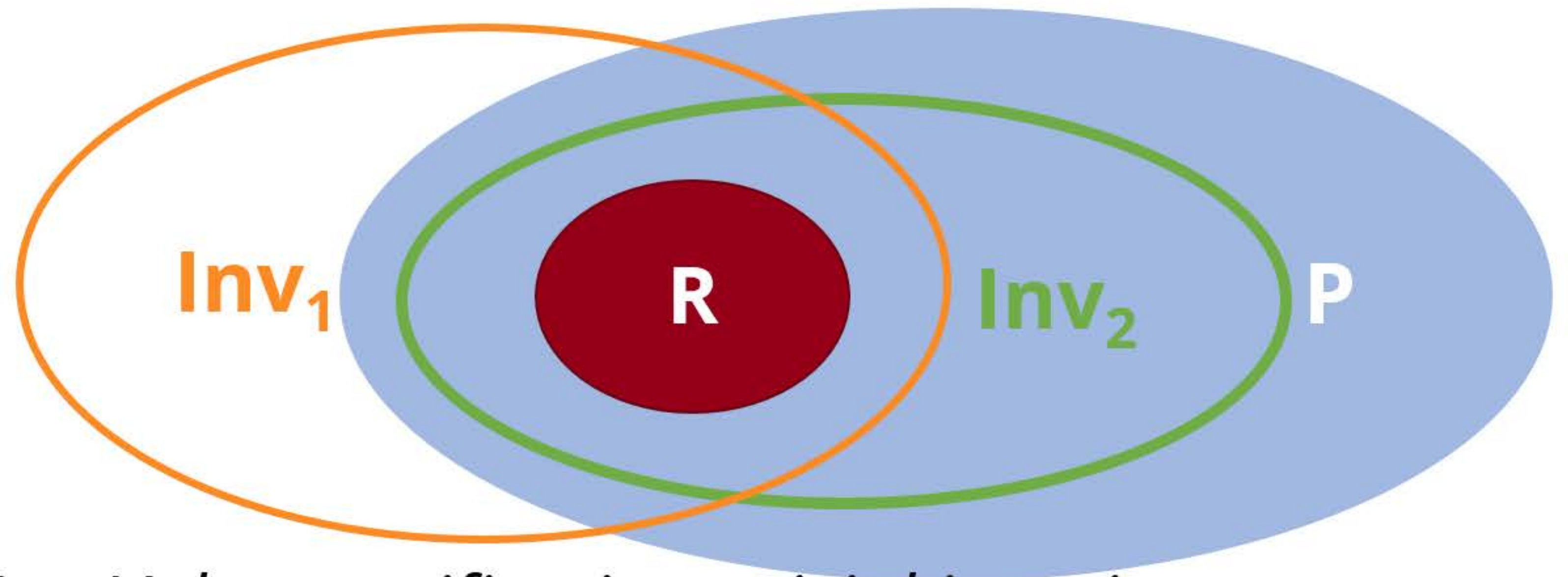
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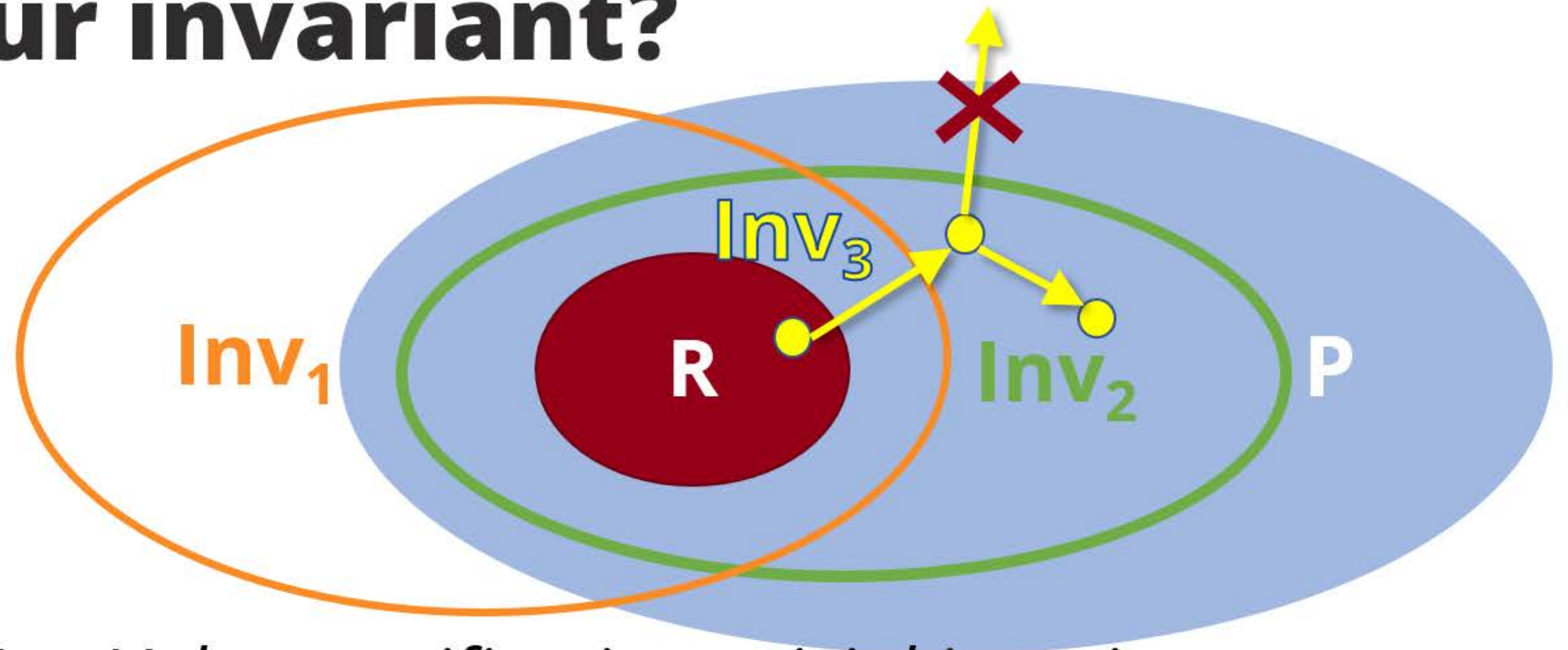
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1. $R(s) \Rightarrow Inv(s)$ *Invariant, without P, not validation*
2. $R(s) \Rightarrow Inv(s) \wedge Inv(s) \Rightarrow P(s)$ *Safe Invariant, second half SAT*

How good is your invariant?



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2. $R(s) \Rightarrow \text{Inv}(s) \wedge \text{Inv}(s) \Rightarrow P(s)$ *Safe Invariant, second half SAT*

3. *Inductive Invariant*

0) $\text{Inv}(s) \Rightarrow P(s)$

1) $I(s) \Rightarrow \text{Inv}(s)$

2) $\text{Inv}(s) \wedge T(s, s') \Rightarrow \text{Inv}(s')$

Example: (1) Invariant Check

$R(s) \Rightarrow \text{Inv}(s)$ - Invariant Check (1)

Basic check for correctness witness

Does not prove anything about P ,
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R(s) => Inv(s) - Invariant Check (1)

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...

```
<node id="N43">
```

```
<data key="invariant">
```

```
state_23 == 200
```

```
</data>
```

```
<data key="invariant.scope">main</data>
```

```
</node>
```

...

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(1..39 is the original Btor2 circuit)
1 sort bitvec 1
2 sort bitvec 20
... ; **remove original property**

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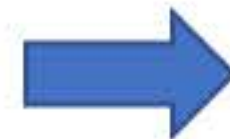
40 constd 2 200 ; **create a constant (200)**

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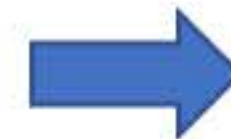
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```
1 sort bitvec 1  
2 sort bitvec 20  
... ; remove original property  
40 constd 2 200 ; create a constant (200)  
41 eq 1 23 40 ; state_23 == 200  
42 neg 1 41 ; negate the property  
43 bad 42  
; bad = „negation of safety property“
```


Full Validation Process

Validation process

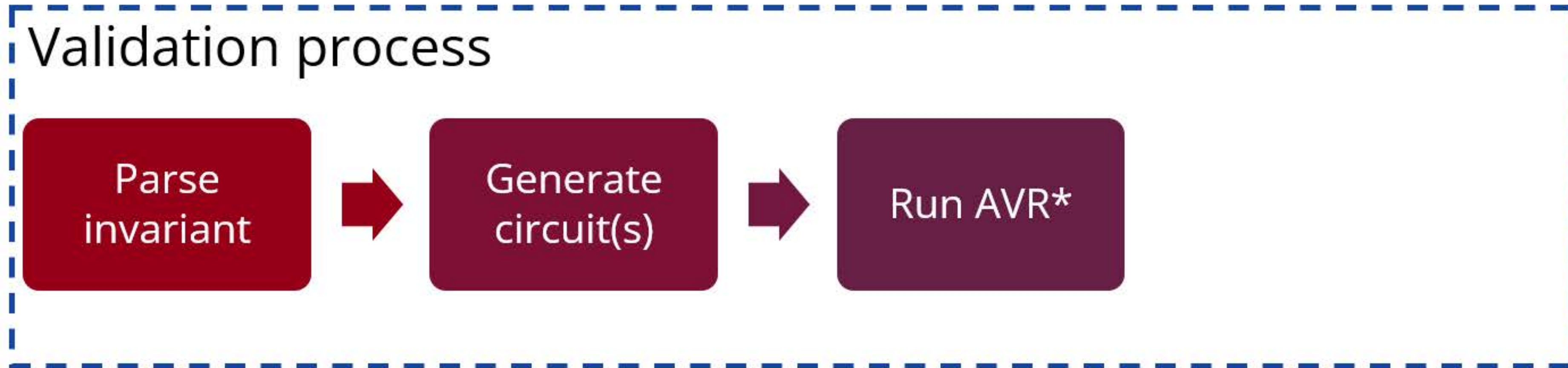
Full Validation Process



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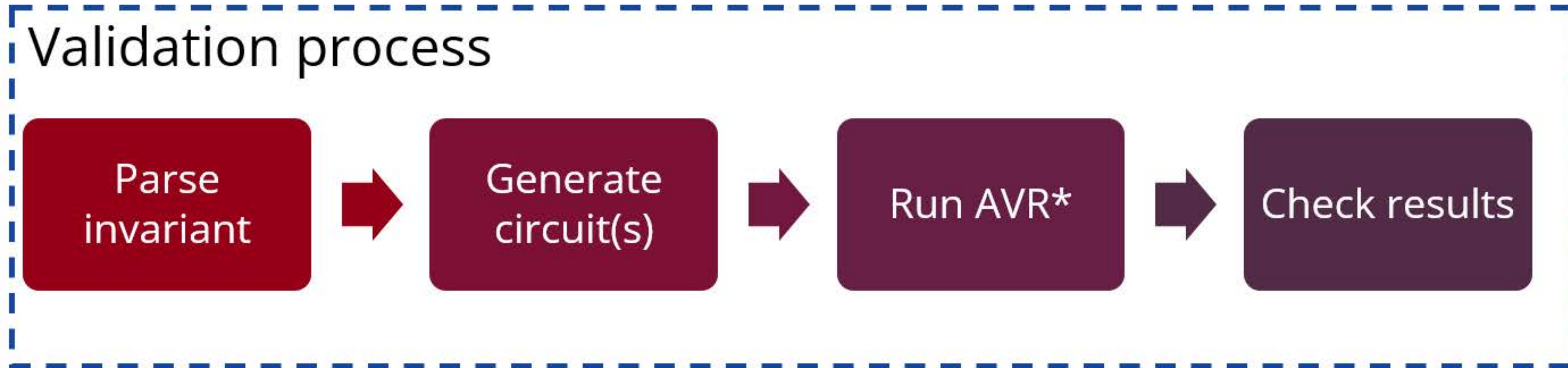


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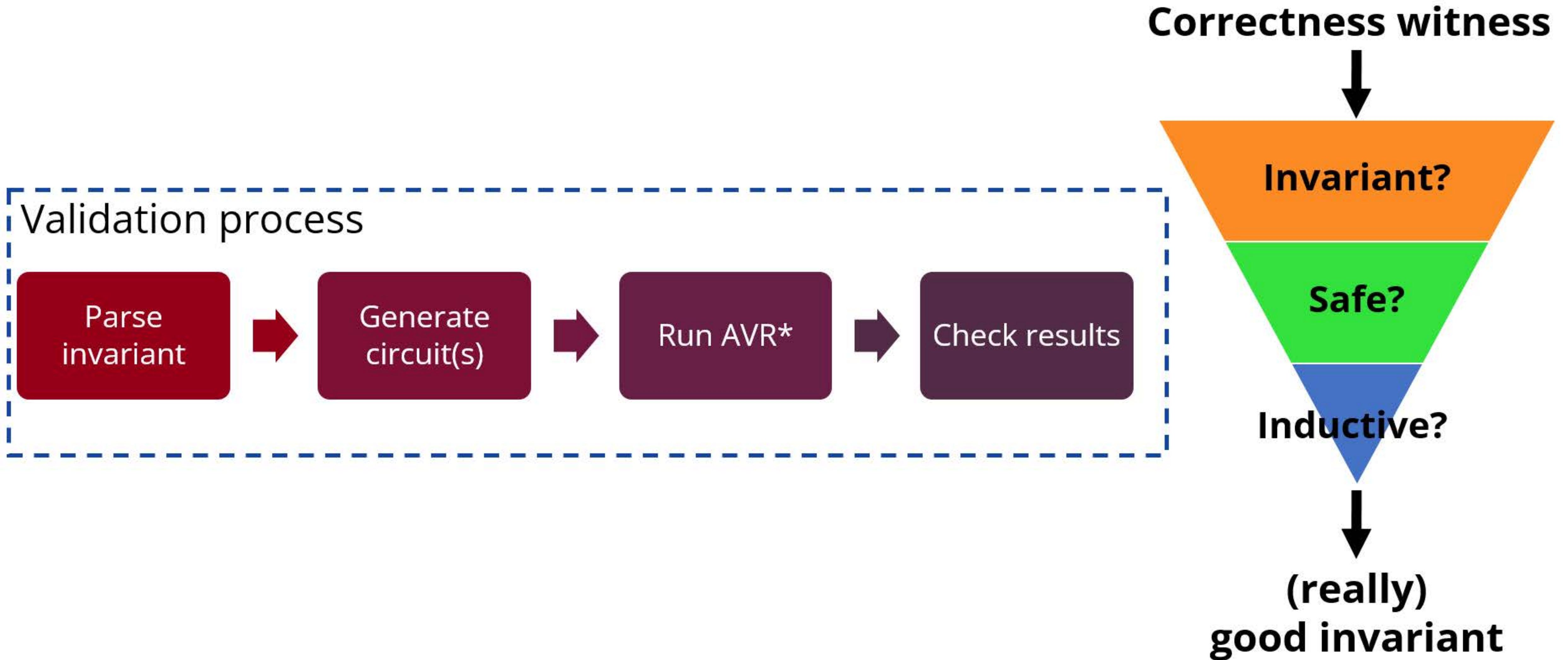
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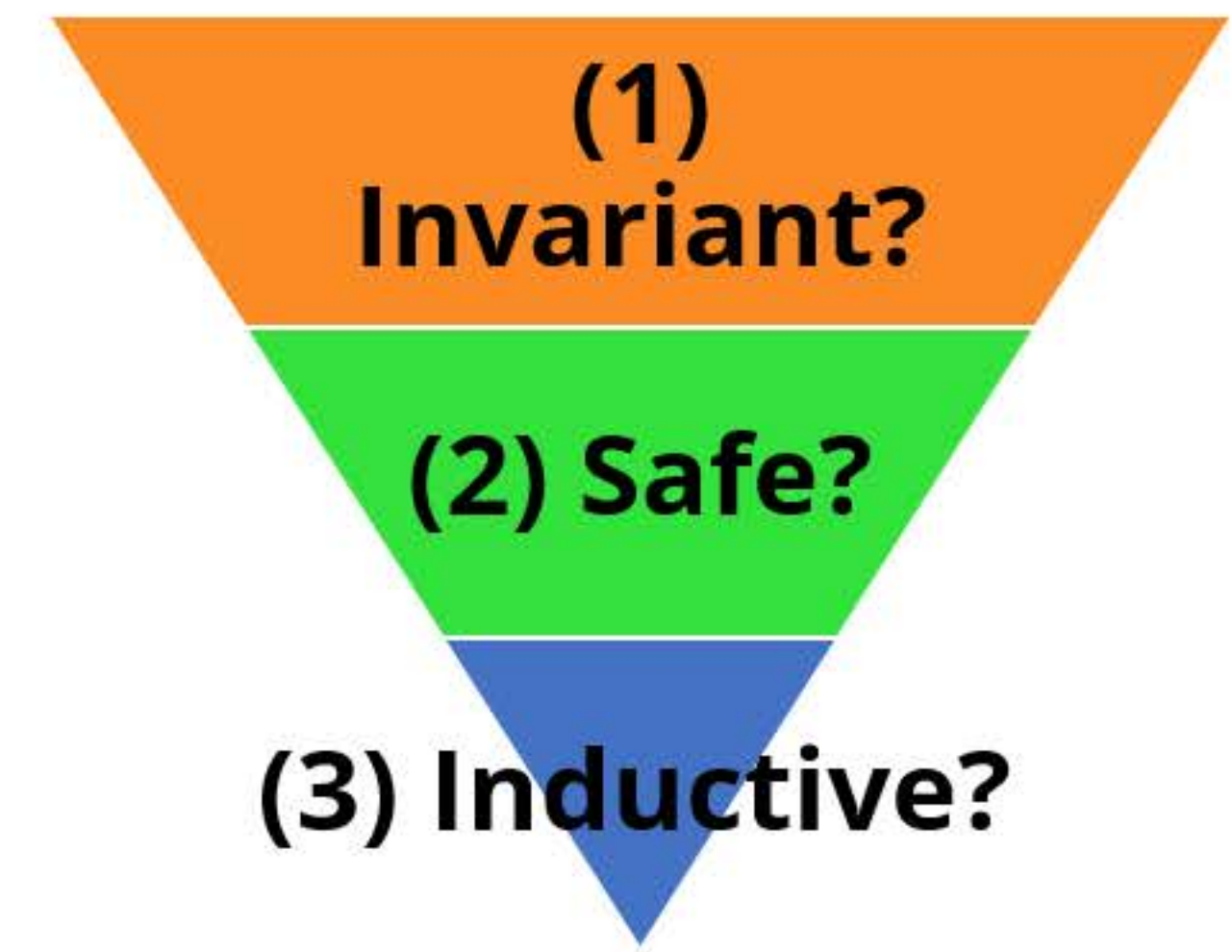
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Results so far

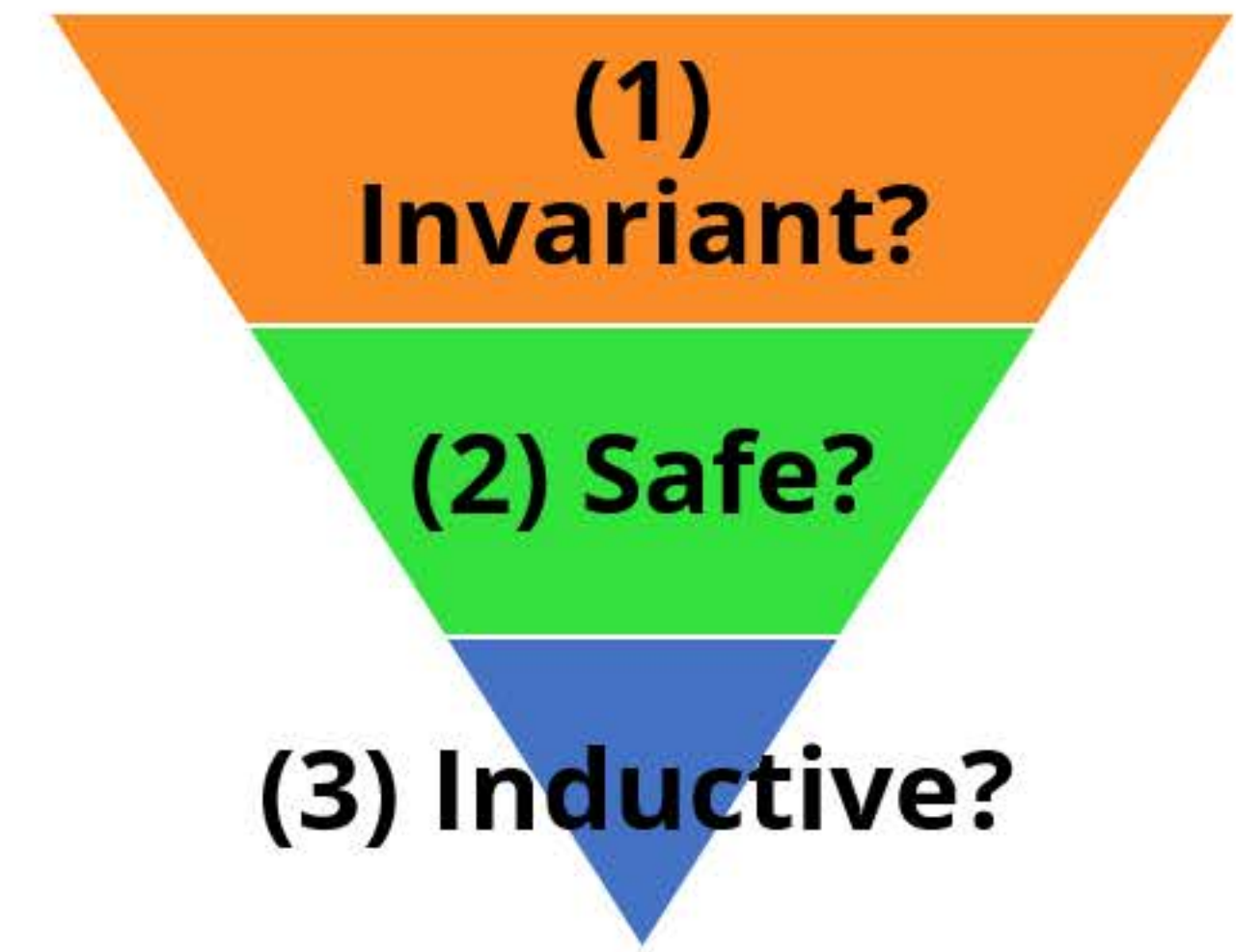
Preliminary Benchmarks

Preliminary Results



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- **867** safe Btor2C benchmarks
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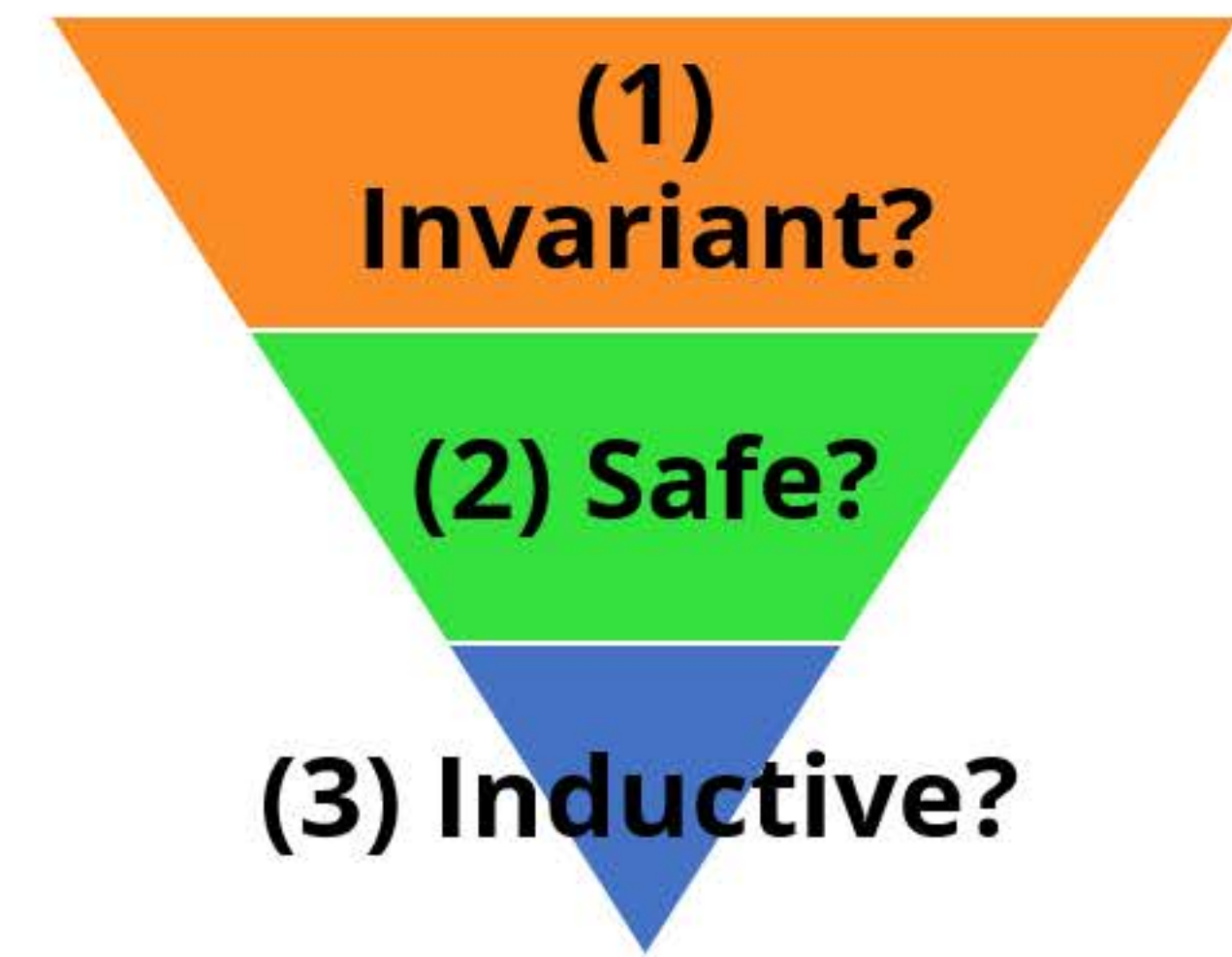


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- **867** safe Btor2C benchmarks
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- **CPAchecker***
 - Predicate abstraction
- **UAutomizer****
 - Default configuration

* cpachecker.sosy-lab.org

** www.ultimate-pa.org

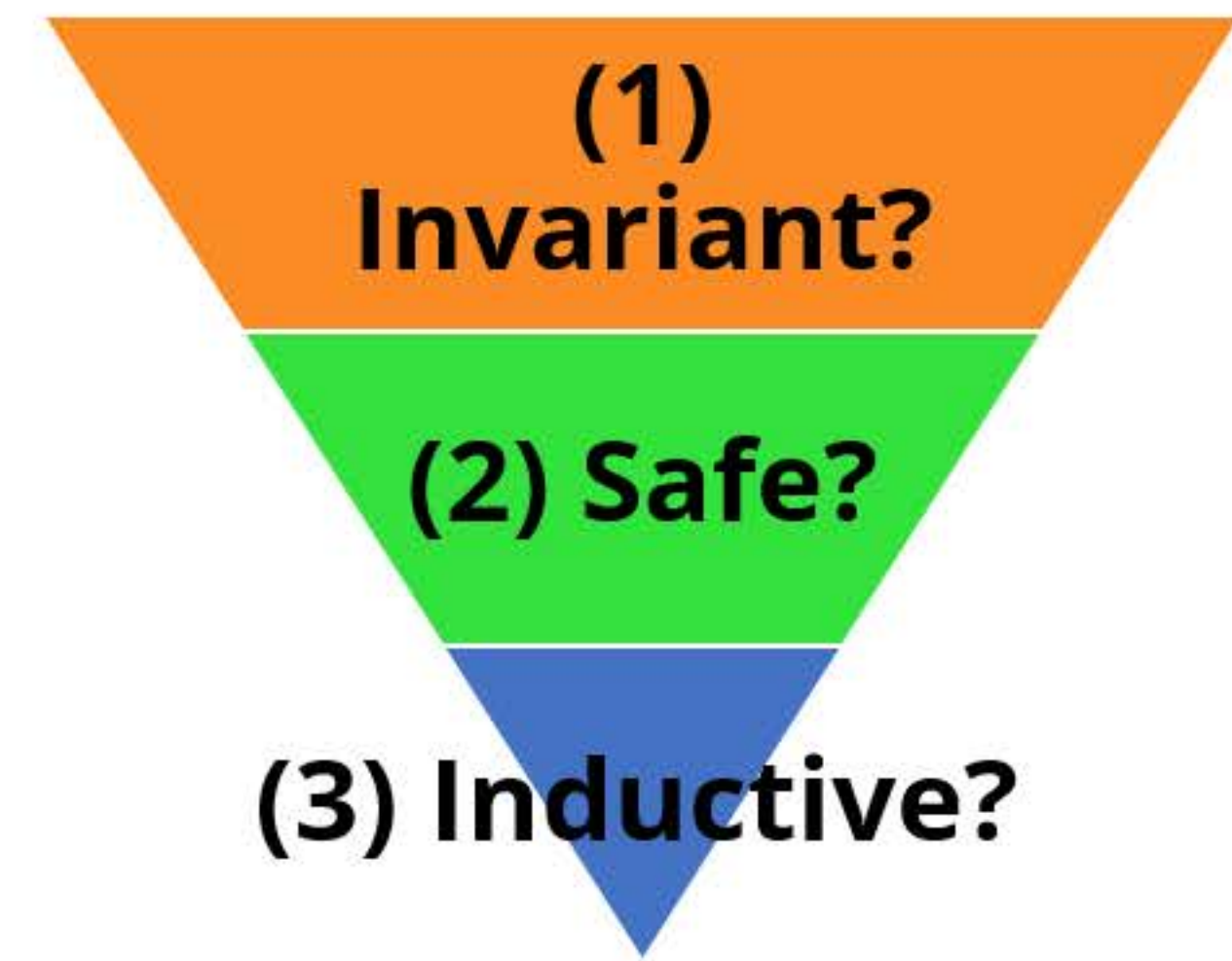


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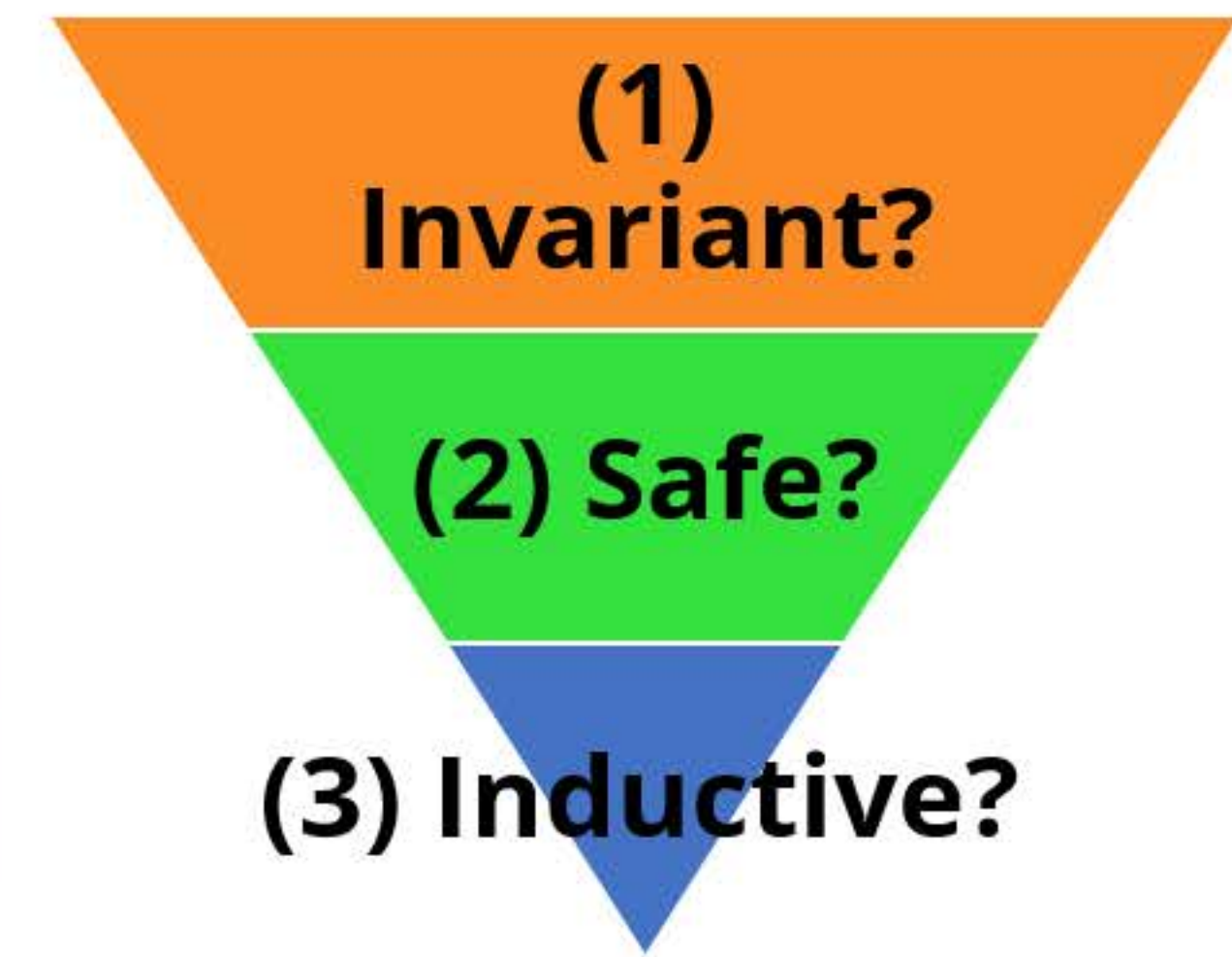
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	UAutomizer	CPAchecker
No invariant	25	7
No valid invariant	42	0
AVR issue	0	78
Timeout	0	25
(1) Invariant only	0	1
(2) Safe only	0	0
(3) Inductive	12	70



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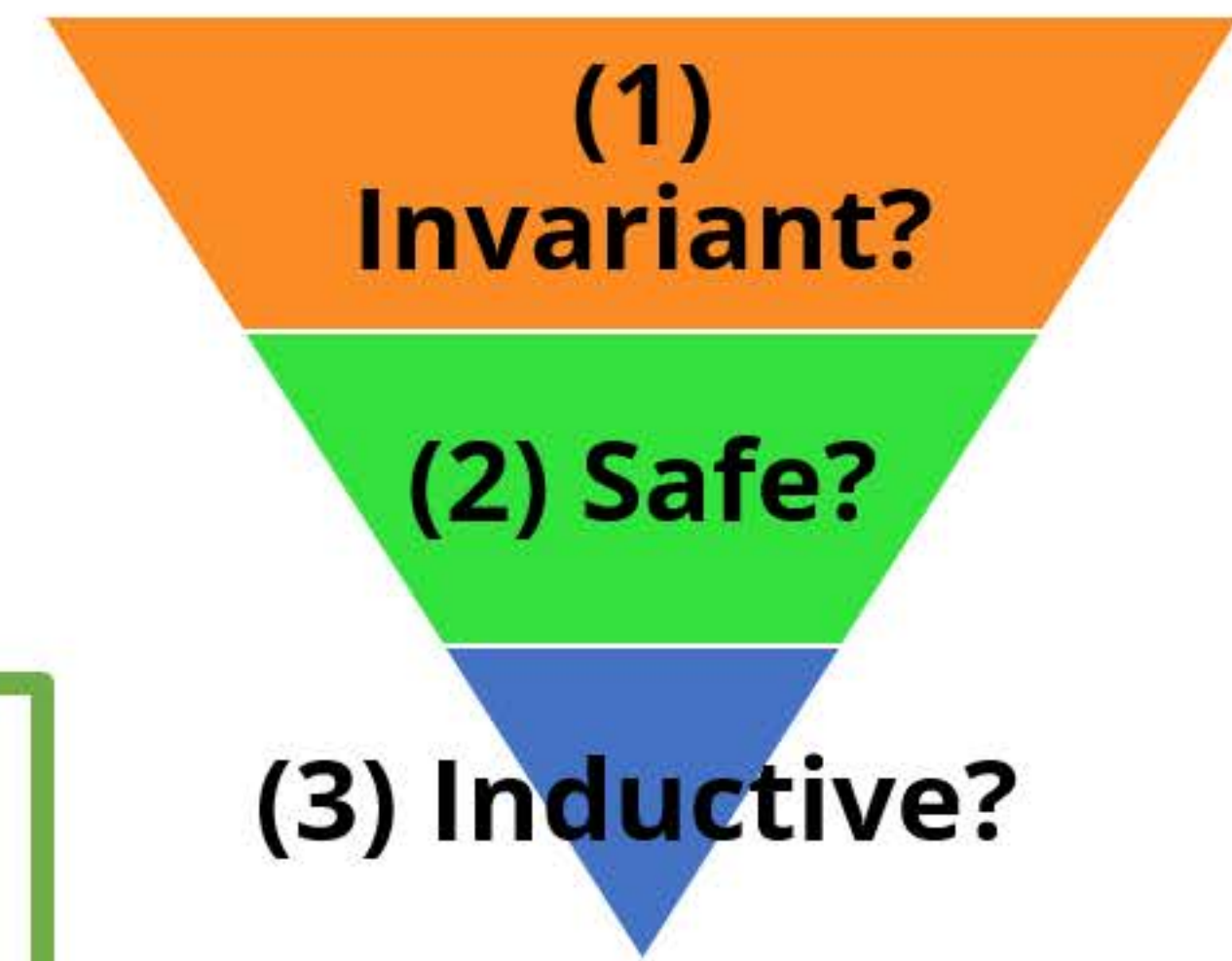
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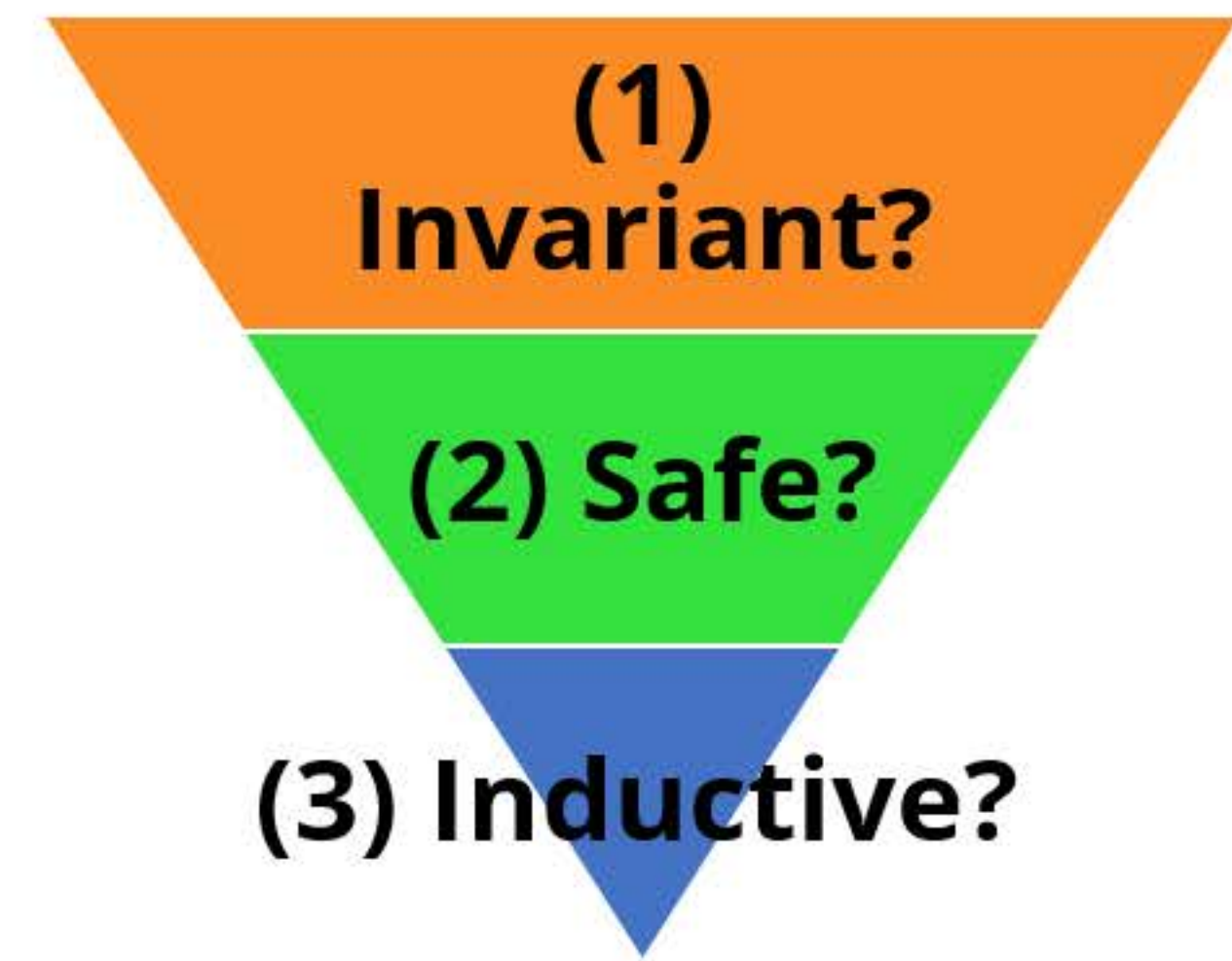
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Known AVR issues,
huge invariants

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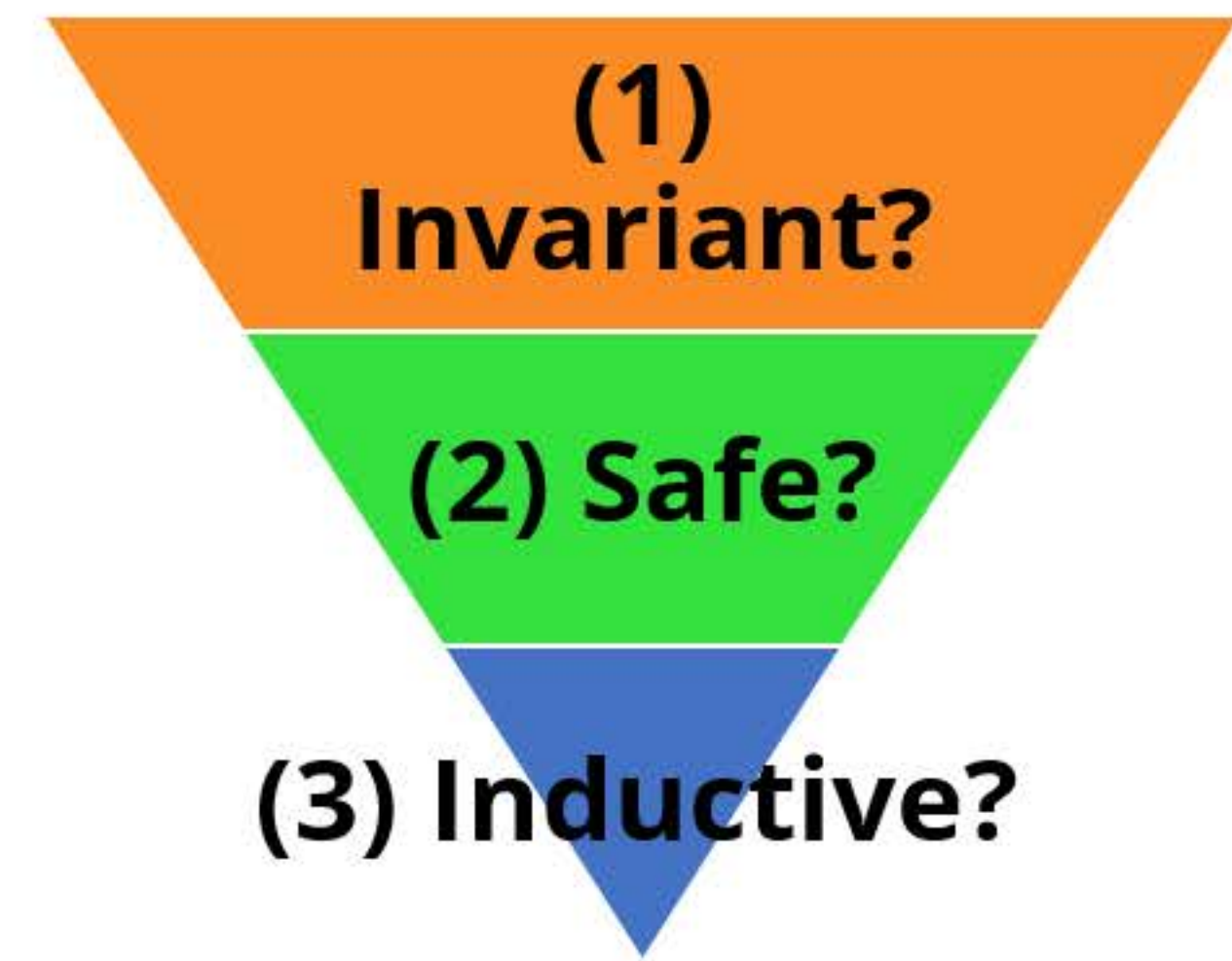
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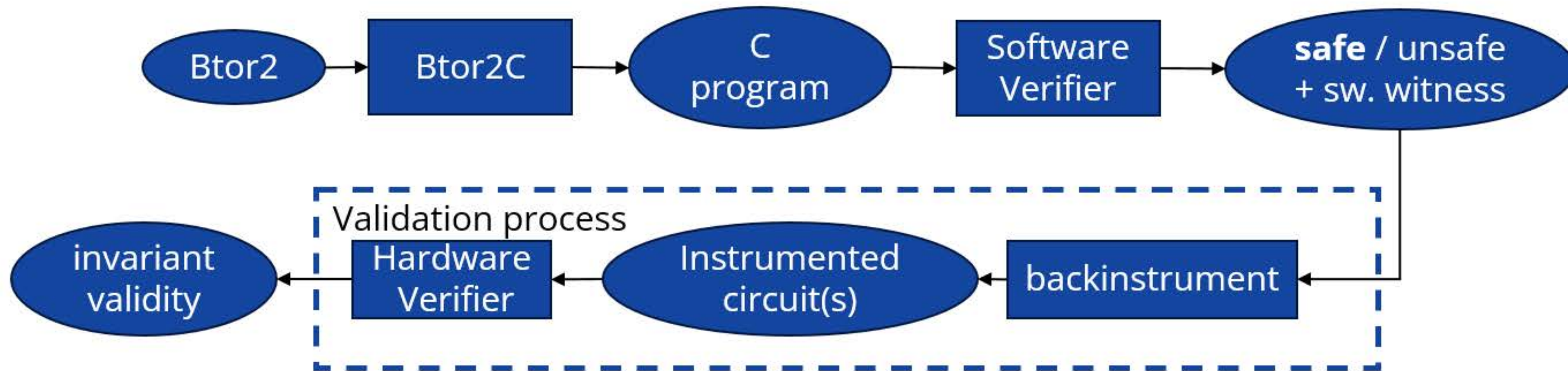
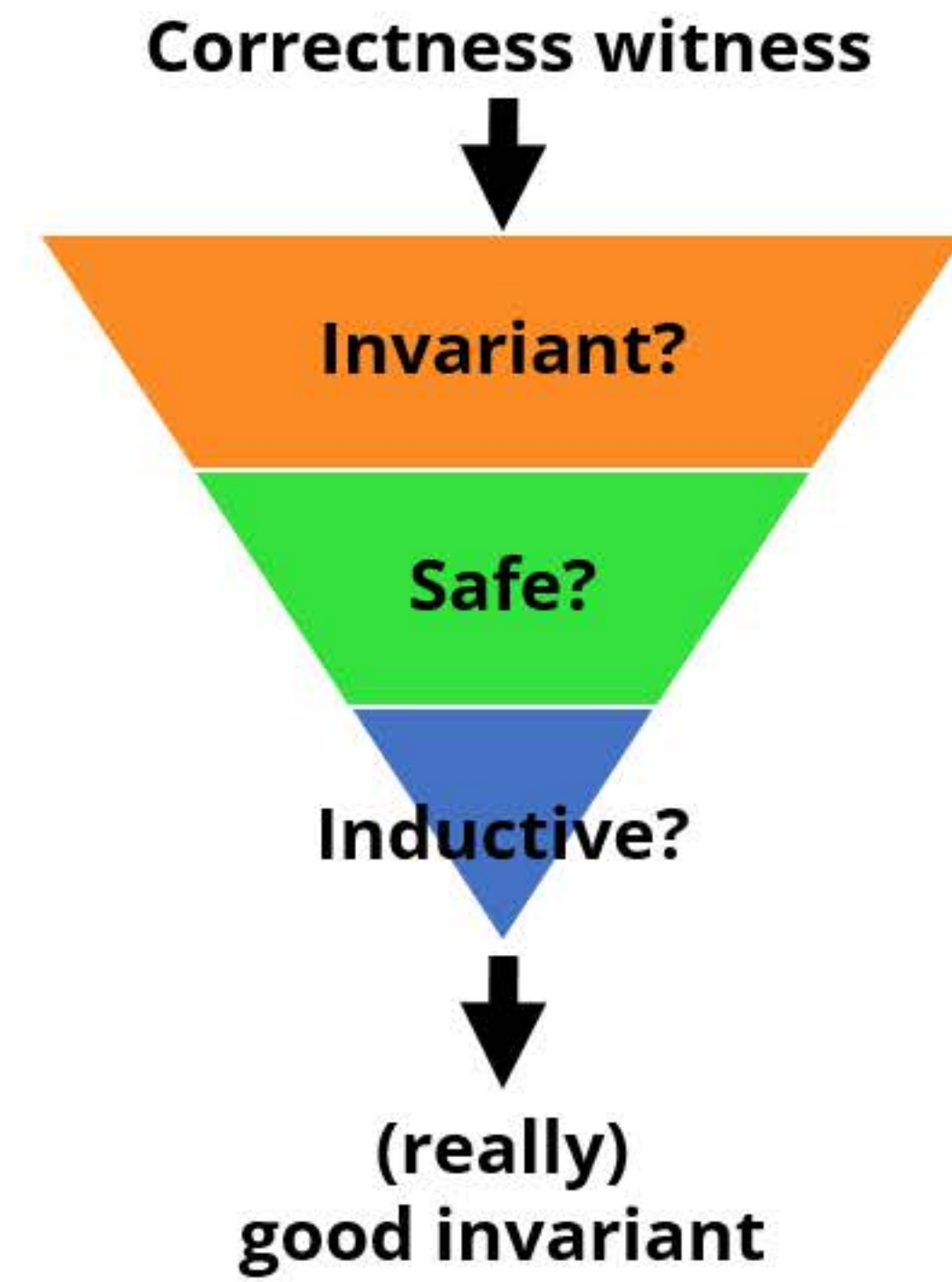
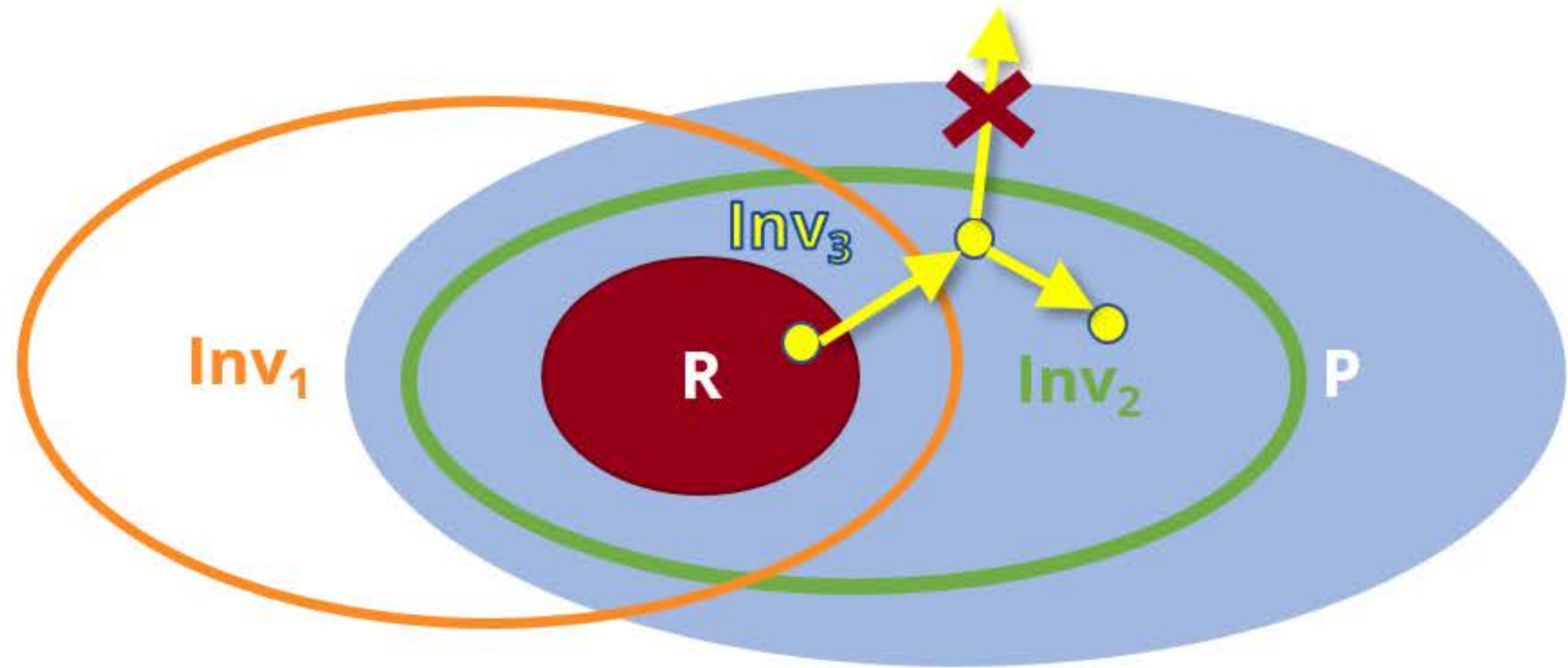
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Good quality
invariants

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Conclusion



Findings, So Far

- **CPAchecker** (cpachecker.sosy-lab.org)
 - Some **missing and invalid invariants** due to (complex) implementation issues
- **Uautomizer** (www.ultimate-pa.org)
 - Some **invalid** (empty or syntax error) invariants
- **2LS** (github.com/diffblue/2ls)
 - Some **trivial or invalid** invariants
- **Btor2C** (gitlab.com/sosy-lab/software/btor2c)
 - Optimization – corner case issues
- **Some fixed already**

Software Witnesses

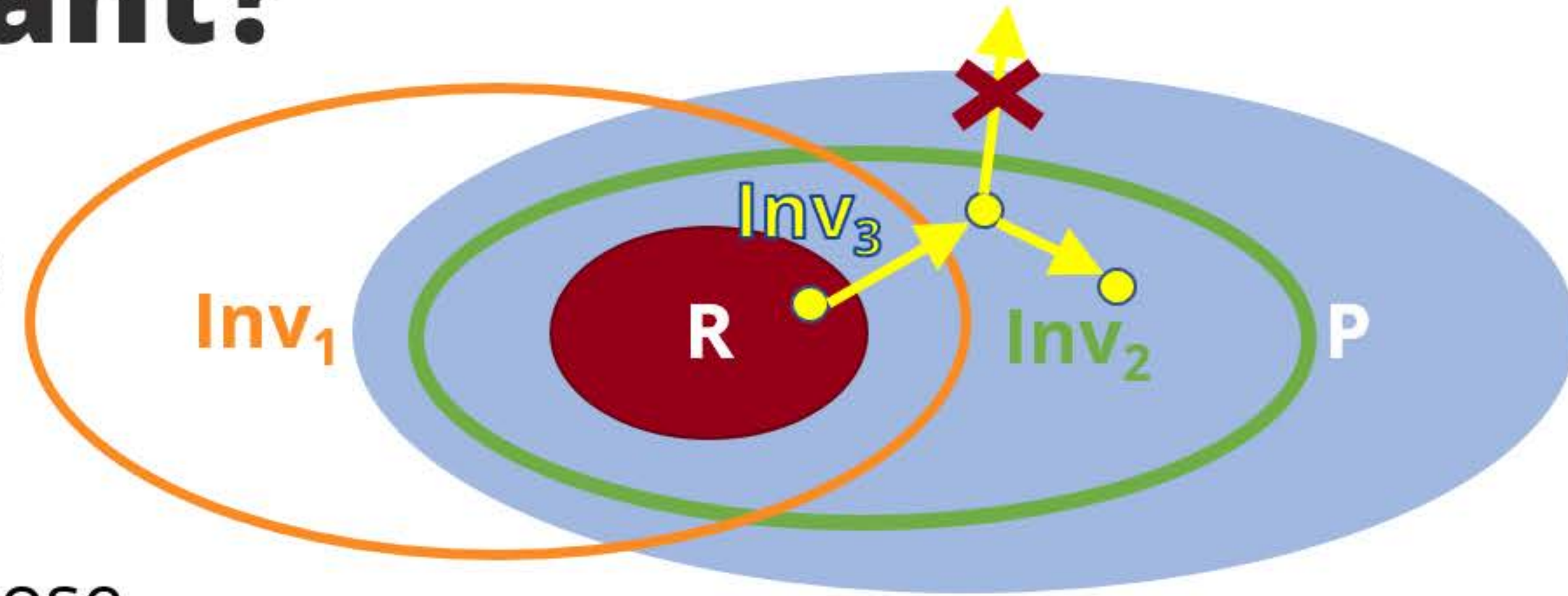
- GraphML
- Violation and **Correctness Witness** formats
- Correctness Witness Automaton:
 - Branching, assumptions, enterLoopHead, ...
 - **Invariants**

```
<node id="N42">
<node id="N43">
  <data key="invariant">
    ( ( ( mask_SORT_1 == 1 ) && ( ( state_23 == 200 )
    || ( ( ( ( ! ( var_10 == ( mask_SORT_4 & state_6 ) ) )
    && ( ( ( ( mask_SORT_1 & var_18 ) == 0 )
    ...
  </data>
  <data key="invariant.scope">main</data>
</node>

<edge source="N42" target="N43">
  <data key="startline">13</data>
  <data key="enterLoopHead">>true</data>
</edge>
```


How good is your invariant?

- $R(s) \Rightarrow P(s) \wedge \text{Inv}(s)$ (0) (*MetaVal*)
 - „loose” check – allows trivial invariants (T)
 - Issue of re-verification
- $R(s) \Rightarrow \text{Inv}(s)$ - Invariant Check (1)
 - Basic check for correctness witness, still loose
 - Does not prove anything about P, but no re-verification
- $R(s) \Rightarrow \text{Inv}(s) \wedge \text{Inv}(s) \Rightarrow P(s)$ - Safe Invariant Check (2)
 - $\text{Inv}(s) \Rightarrow P(s)$ is a SAT problem, not model checking
- Inductive Invariant Check (3)
 - 0) $\text{Inv}(s) \Rightarrow P(s)$
 - 1) $I(s) \Rightarrow \text{Inv}(s)$
 - 2) $\text{Inv}(s) \wedge T(s, s') \Rightarrow \text{Inv}(s')$
 - Pure SAT solving
 - Mature HW algorithms return inductive invariants (*IMC, PDR*)

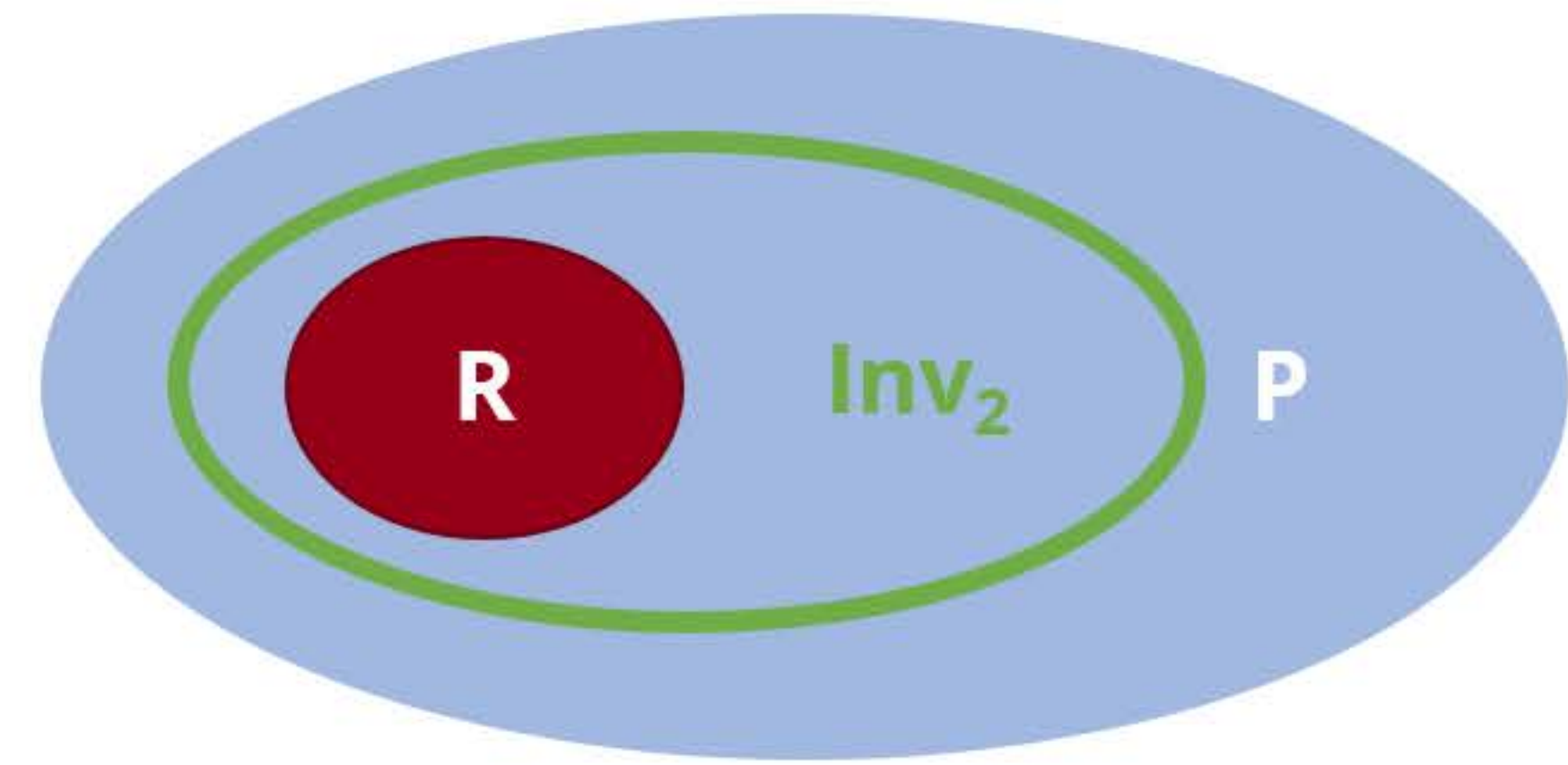


(2) Safe Invariant Check

$$R(s) \Rightarrow \text{Inv}(s) \wedge \text{Inv}(s) \Rightarrow P(s)$$

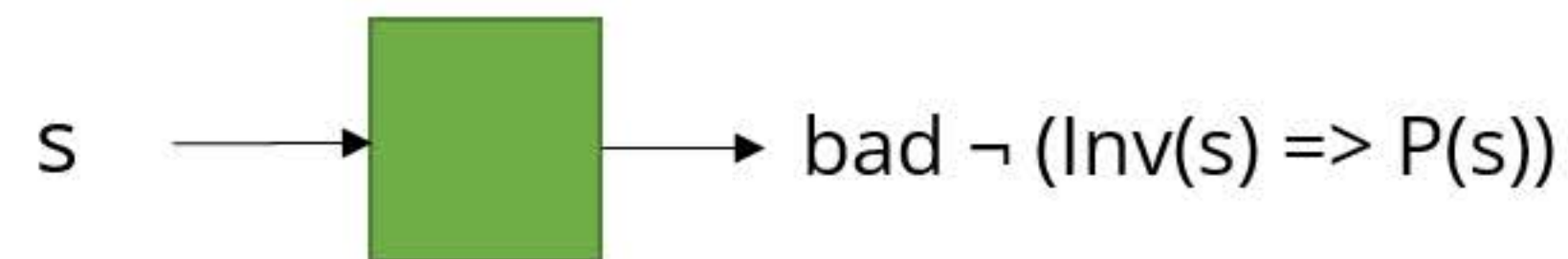
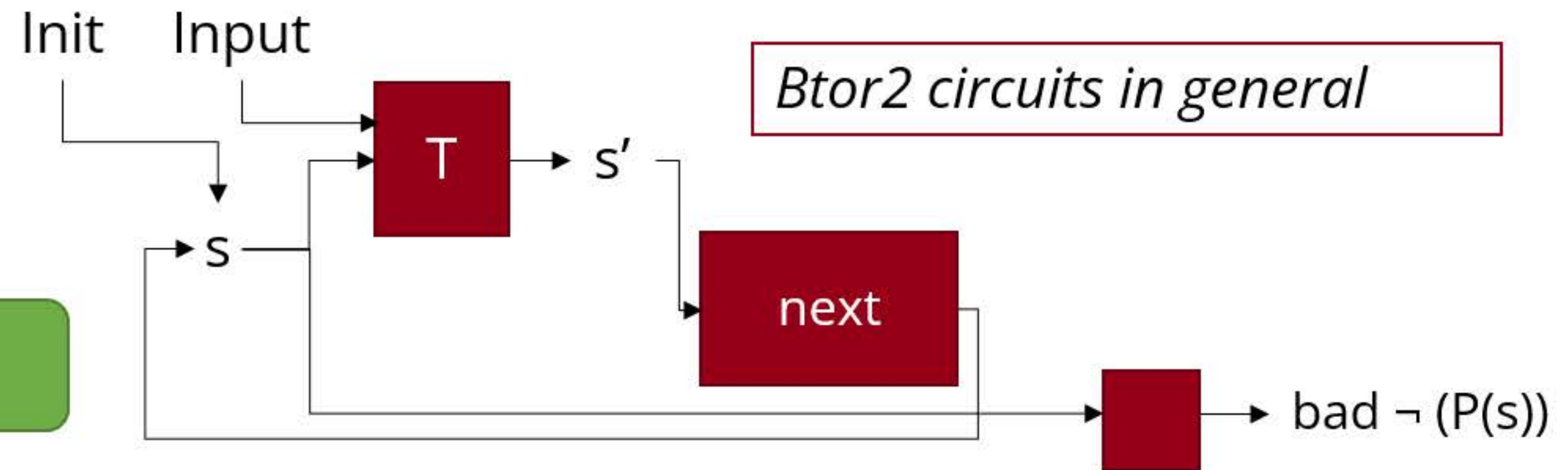
Safe Invariant Check

(Inv(s) => P(s) is SAT solving only)



How to check

- Do check (1) – $R(s) \Rightarrow \text{Inv}(s)$
- $\text{Inv}(s) \Rightarrow P(s)$
 - Add new property:
 $\neg(\text{Inv}(s) \Rightarrow P(s))$
 - Remove **next**, **init**
 - Remove property $\neg P(s)$



(3) Inductive Invariant Check

(Safe and) Inductive Invariant Check

0) $Inv(s) \Rightarrow P(s)$

1) $I(s) \Rightarrow Inv(s)$

2) $Inv(s) \wedge T(s, s') \Rightarrow Inv(s')$

How to do

0) see check (2)

1) remove **next**,
change property to $\neg Inv(s)$

2) remove **next**, **init**,
change property to
 $\neg (Inv(s) \Rightarrow Inv(s'))$

